

**THE UNIVERSITY OF TEXAS AT AUSTIN
DEPARTMENT OF ELECTRICAL ENGINEERING
EE 360K: DIGITAL SYSTEMS LAB**

Experiment 8.1 – Implementing a Combinational Logic Circuit with Logic Gates, Multiplexers, and Registers

1. Introduction: This circuit is intended as a classroom exercise in which, beyond logic and computer-aided logic, you will be implementing a circuit with logic gates using in the lab. You will build a combinational logic circuit that takes up an 8-bit binary number and outputs the 8-bit binary number shifted right by one bit.

2. Objectives: The purpose of Experiment 8.1 is to implement a circuit with the following specifications: when the 8-bit binary number is input, the output will be the 8-bit binary number shifted right by one bit.

3. Introduction:

We will implement a circuit that takes an 8-bit binary number and outputs the 8-bit binary number shifted right by one bit.

4. Implementation: The circuit will be implemented using the following components: 8-bit binary number, 8-bit register, 8-bit multiplexer, and 8-bit register.

- 1. 8-bit binary number (input)
- 2. 8-bit register (output)
- 3. 8-bit multiplexer (output)
- 4. 8-bit register (output)
- 5. 8-bit register (output)
- 6. 8-bit register (output)
- 7. 8-bit register (output)
- 8. 8-bit register (output)

5. Introduction:

- 1. The circuit will be implemented using the following components:
- 2. The circuit will be implemented using the following components:
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Answers For Digital Fundamentals Experiment

City of London College of Economics



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1. The book is structured into several chapters, namely:
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 - Chapter 2: Essential Elements of Answers For Digital Fundamentals Experiment
 - Chapter 3: Answers For Digital Fundamentals Experiment in Everyday Life
 - Chapter 4: Answers For Digital Fundamentals Experiment in Specific Contexts
 - Chapter 5: Conclusion
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- The book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of Answers For Digital Fundamentals Experiment.

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