

5.

Applying KCL at node X

$$\frac{V_x}{2R} + \frac{V_x - V_A}{2R} + \frac{V_x - V_y}{2R} = 0$$

$$\frac{V_x + V_x - V_A + 2V_x - 2V_y}{2R} = 0$$

$$4V_x - 2V_y - V_A = 0 \Rightarrow \textcircled{1}$$

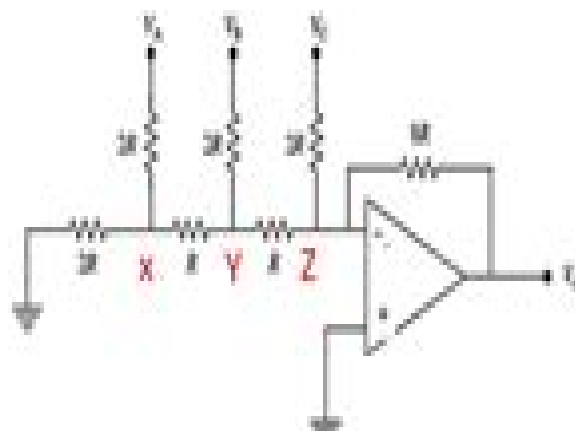
Applying KCL at node Y

$$\frac{V_y - V_x}{R} + \frac{V_y - V_B}{2R} + \frac{V_y - 0}{R} = 0$$

$$\therefore V_Z = 0$$

$$\frac{2V_y - 2V_x + V_y - V_B + 2V_y}{2R} = 0$$

$$-2V_x + 5V_y - V_B = 0 \Rightarrow \textcircled{2}$$



# Analog Electronics Solved Problems

**Vančo B. Litovski**



## **Analog Electronics Solved Problems:**

**Lecture Notes in Analog Electronics** Vančo B. Litovski, 2025-01-17 Prof Van o Litovski was born in 1947 in Rakita South Macedonia Greece He graduated from the Faculty of Electronic Engineering in Ni in 1970 and obtained his M Sc in 1974 and his Ph D in 1977 He was appointed as a teaching assistant at the Faculty of Electronic Engineering in 1970 and became a full professor at the same faculty in 1987 He was elected as a visiting professor honoris causa at the University of Southampton in 1999 From 1987 until 1990 he was a consultant to the CEO of Ei and was the head of the Chair of Electronics at the Faculty of Electronic Engineering in Ni for 12 years From 2015 to 2017 he was a researcher at the University of Bath He received several awards including from the Faculty of Electronic Engineering Charter in 1980 Charter in 1985 and a Special Recognition in 1995 and the University of Ni Plaque 1985 *Lecture Notes in Analog Electronics* Vančo Litovski, 2024-02-23 This book discusses unified noise models of the broadest set of electronic components including resistors diodes all types of transistors and most types of opto electronic devices The noise however is a phenomenon which is inherent to any technology It is omnipresent It is obstructing every application and in many cases special actions must be undertaken to recognize the main function s signal in the mistiness of the noise The number of types of noise sources in electronics is almost unlimited The book offers unique comprehensive approach to noise analysis in electronic circuits based on modified nodal analysis and the superposition theorem It also encompasses a broadest set of low noise amplifier design procedures covering BJT MOSFET MESFET and HEMT technologies Analog Electronics Balwinder Singh, Ashish Dixit, 2007

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