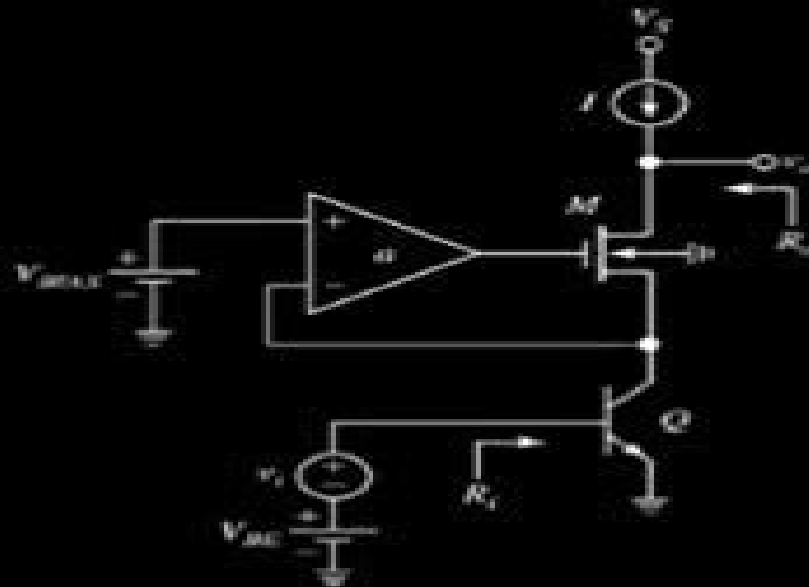


Analog Circuit Design

Discrete & Integrated



Sergio Franco

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Circuit Design: Discrete & Integrated Sergio Franco, 2014-01-22 Analog Circuit Design Discrete and Integrated is written by enthusiastic circuit practitioner Sergio Franco. This text places great emphasis on developing intuition and physical insight. The numerous examples and problems have been carefully thought out to promote problem solving methodologies of the type engineers apply daily on the job. Each chapter provides a fairly comprehensive coverage of its title subject. SPICE has been integrated throughout the text both as a pedagogical aid to confer more immediately to a new concept and as a validation tool for hand calculations. PSPICE is used to bring out nuances that would be too complex for hand calculations.

Scientific and Technical Books and Serials in Print, 1989 **The British National Bibliography** Arthur James Wells, 1968 *Analysis and Design of Analog Integrated Circuits* Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2024-01-04 ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS Authoritative and comprehensive textbook on the fundamentals of analog integrated circuits with learning aids included throughout. Written in an accessible style to ensure complex content can be appreciated by both students and professionals, this Sixth Edition of Analysis and Design of Analog Integrated Circuits is a highly comprehensive textbook on analog design offering in-depth coverage of the fundamentals of circuits in a single volume. To aid in reader comprehension and retention, supplementary material includes end-of-chapter problems plus a Solution Manual for instructors. In addition to the well-established concepts, this Sixth Edition introduces a new super source follower circuit and its large signal behavior, frequency response, stability, and noise properties. New material also introduces replica biasing, describes and analyzes two op amps with replica biasing, and provides coverage of weighted zero value time constants as a method to estimate the location of dominant zeros, pole-zero doublets, including their effect on settling time, and three examples of circuits that create doublets, the effect of feedback on pole-zero doublets, and MOS transistor noise performance, including a thorough treatment on thermally induced gate noise. Providing complete coverage of the subject, Analysis and Design of Analog Integrated Circuits serves as a valuable reference for readers from many different types of backgrounds, including senior undergraduates and first-year graduate students in electrical and computer engineering, along with analog integrated circuit designers.

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