

III Semester B.E. (E&E) Examination, Dec. 2018/Jan. 2019
(2K11 Scheme)

EE 301 : ANALOG ELECTRONIC CIRCUITS

Time : 3 Hours

Max. Marks : 100

Instruction : Answer any five full questions, by choosing atleast two from each Part.

PART – A

1. a) Classify the diode clipping circuits with neat circuits and relevant waveforms. 8
- b) For the circuit shown below figure 1. b) sketch the transfer characteristic and o/p waveform (Assume ideal diodes). 6

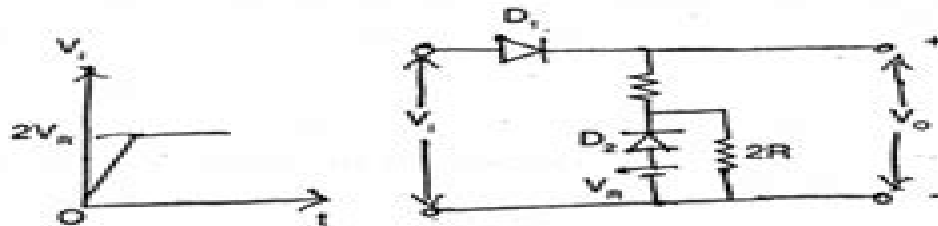


Fig. 1(b)

- c) Define clamping circuit and also explain the working of positive peak clamper. 6
2. a) Define Q-point. Discuss the issues related to stability of operating point of BJT amplifiers. 6
- b) Compare and contrast stability factor of fixed-bias and self-biased BJT circuits. 6
- c) Design a voltage divider bias circuit using a silicon transistor with $V_{CC} = 18V$, $I_C = 2.3 \text{ mA}$, $V_{CE} = 8.2 \text{ V}$, $R_C = 3.3 \text{ K}$, $\beta = 100$ and $S(I_{\infty}) \leq 5$. 8

Analog Electronic Circuit 3rd Sem Ec

Michael Hoelscher



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Analog Electronic Circuits (For 3rd Semester of APJKTU, Kerala) Sukumaran P., Analog Electronic Circuits

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