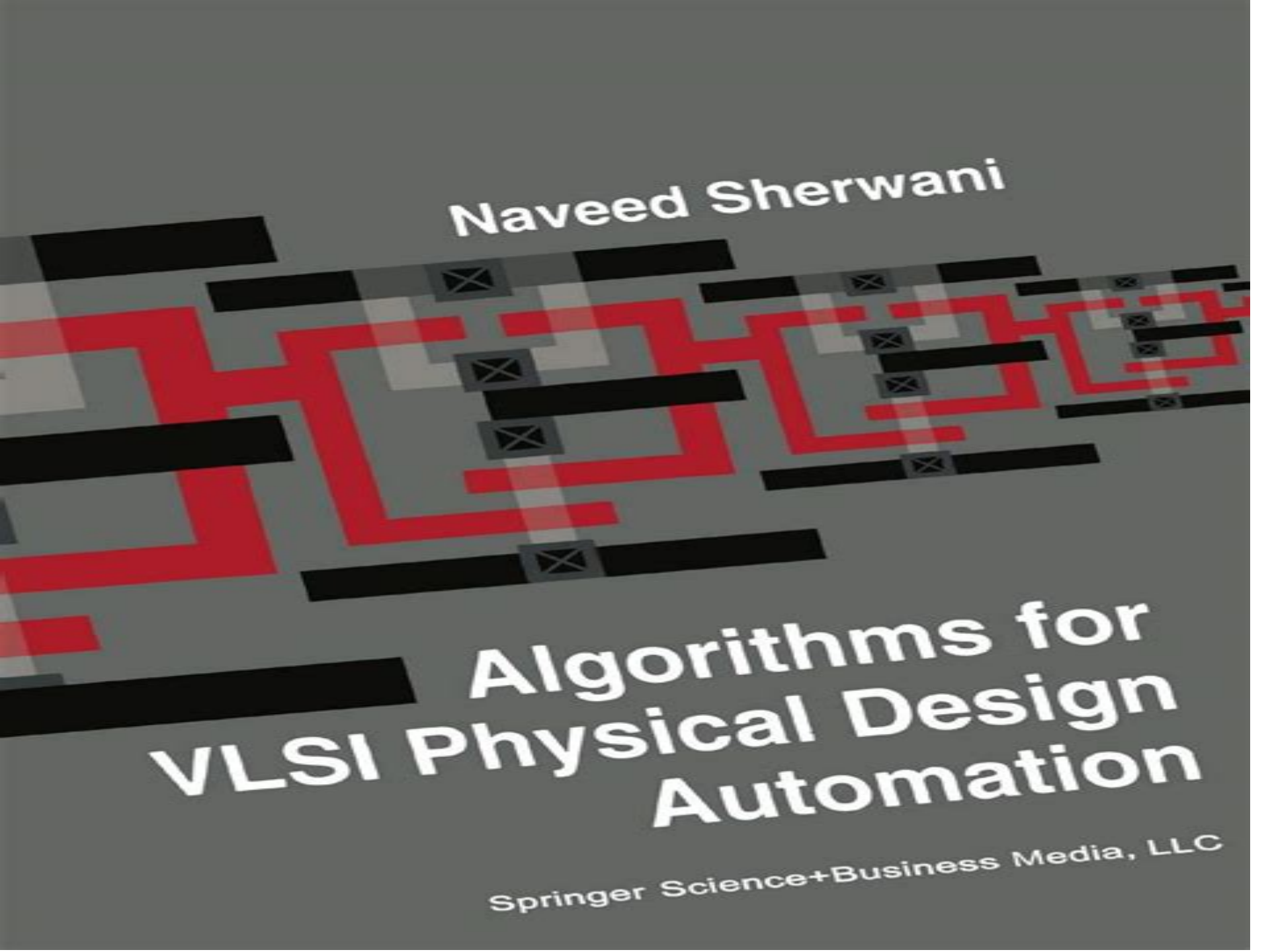




Naveed Sherwani

Algorithms for VLSI Physical Design Automation

Springer Science+Business Media, LLC

Algorithms For Vlsi Design Automation Naveed Sherwani

Teresa H. Meng, Sharad Malik



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Algorithms for VLSI Physical Design Automation Naveed A. Sherwani, 2012-12-06 Algorithms for VLSI Physical Design Automation Second Edition is a core reference text for graduate students and CAD professionals Based on the very successful First Edition it provides a comprehensive treatment of the principles and algorithms of VLSI physical design presenting the concepts and algorithms in an intuitive manner Each chapter contains 3 4 algorithms that are discussed in detail Additional algorithms are presented in a somewhat shorter format References to advanced algorithms are presented at the end of each chapter Algorithms for VLSI Physical Design Automation covers all aspects of physical design In 1992 when the First Edition was published the largest available microprocessor had one million transistors and was fabricated using three metal layers Now we process with six metal layers fabricating 15 million transistors on a chip Designs are moving to the 500 700 MHz frequency goal These stunning developments have significantly altered the VLSI field over the cell routing and early floorplanning have come to occupy a central place in the physical design flow This Second Edition introduces a realistic picture to the reader exposing the concerns facing the VLSI industry while maintaining the theoretical flavor of the First Edition New material has been added to all chapters new sections have been added to most chapters and a few chapters have been completely rewritten The textual material is supplemented and clarified by many helpful figures Audience An invaluable reference for professionals in layout design automation and physical design *Algorithms for VLSI Physical Design Automation* Naveed A. Sherwani, 2007-05-08 Algorithms for VLSI Physical Design Automation Third Edition covers all aspects of physical design The book is a core reference for graduate students and CAD professionals For students concepts and algorithms are presented in an intuitive manner For CAD professionals the material presents a balance of theory and practice An extensive bibliography is provided which is useful for finding advanced material on a topic At the end of each chapter exercises are provided which range in complexity from simple to research level Algorithms for VLSI Physical Design Automation Third Edition provides a comprehensive background in the principles and algorithms of VLSI physical design The goal of this book is to serve as a basis for the development of introductory level graduate courses in VLSI physical design automation It provides self contained material for teaching and learning algorithms of physical design All algorithms which are considered basic have been included and are presented in an intuitive manner Yet at the same time enough detail is provided so that readers can actually implement the algorithms given in the text and use them The first three chapters provide the background material while the focus of each chapter of the rest of the book is on each phase of the physical design cycle In addition newer topics such as physical design automation of FPGAs and MCMs have been included The basic purpose of the third edition is to investigate the new challenges presented by interconnect and process innovations In 1995 when the second edition of this book was prepared a six layer process and 15 million transistor microprocessors were in advanced stages of design In 1998 six metal process and 20 million transistor designs are in production Two new chapters

have been added and new material has been included in almost all other chapters. A new chapter on process innovation and its impact on physical design has been added. Another focus of the third edition is to promote use of the Internet as a resource so wherever possible URLs have been provided for further investigation. Algorithms for VLSI Physical Design Automation Third Edition is an important core reference work for professionals as well as an advanced level textbook for students.

Asynchronous Circuit Design for VLSI Signal Processing Teresa H. Meng, Sharad Malik, 2011-06-27 Asynchronous Circuit Design for VLSI Signal Processing is a collection of research papers on recent advances in the area of specification design and analysis of asynchronous circuits and systems. This interest in designing digital computing systems without a global clock is prompted by the ever growing difficulty in adopting global synchronization as the only efficient means to system timing. Asynchronous circuits and systems have long held interest for circuit designers and researchers alike because of the inherent challenge involved in designing these circuits as well as developing design techniques for them. The frontier research in this area can be traced back to Huffman's publications The Synthesis of Sequential Switching Circuits in 1954 followed by Unger's book Asynchronous Sequential Switching Circuits in 1969 where a theoretical foundation for handling logic hazards was established. In the last few years a growing number of researchers have joined force in unveiling the mystery of designing correct asynchronous circuits and better yet have produced several alternatives in automatic synthesis and verification of such circuits. This collection of research papers represents a balanced view of current research efforts in the design synthesis and verification of asynchronous systems.

Physical Design Essentials Khosrow Golshan, 2007-04-08 Arranged in a format that follows the industry common ASIC physical design flow Physical Design Essentials begins with general concepts of an ASIC library then examines floorplanning placement routing verification and finally testing. Among the topics covered are Basic standard cell design transistor sizing and layout styles Linear non linear and polynomial characterization Physical design constraints and floorplanning styles Algorithms used for placement Clock Tree Synthesis Parasitic extraction Electronic Testing and many more.

Multiscale Optimization Methods and Applications William W. Hager, Shu-Jen Huang, Panos M. Pardalos, Oleg A. Prokopyev, 2006-06-18 As optimization researchers tackle larger and larger problems scale interactions play an increasingly important role. One general strategy for dealing with a large or difficult problem is to partition it into smaller ones which are hopefully much easier to solve and then work backwards towards the solution of original problem using a solution from a previous level as a starting guess at the next level. This volume contains 22 chapters highlighting some recent research. The topics of the chapters selected for this volume are focused on the development of new solution methodologies including general multilevel solution techniques for tackling difficult large scale optimization problems that arise in science and industry. Applications presented in the book include but are not limited to the circuit placement problem in VLSI design a wireless sensor location problem optimal dosages in the treatment of cancer by radiation therapy and facility location.

Layout Optimization in VLSI Design Bing Lu, Ding-Zhu

Du, S. Sapatnekar, 2013-06-29 Introduction The exponential scaling of feature sizes in semiconductor technologies has side effects on layout optimization related to effects such as interconnect delay noise and crosstalk signal integrity parasitics effects and power dissipation that invalidate the assumptions that form the basis of previous design methodologies and tools This book is intended to sample the most important contemporary and advanced layout optimization problems emerging with the advent of very deep submicron technologies in semiconductor processing We hope that it will stimulate more people to perform research that leads to advances in the design and development of more efficient effective and elegant algorithms and design tools Organization of the Book The book is organized as follows A multi stage simulated annealing algorithm that integrates floorplanning and interconnect planning is presented in Chapter 1 To reduce the run time different interconnect planning approaches are applied in different ranges of temperatures Chapter 2 introduces a new design methodology the interconnect centric design methodology and its centerpiece interconnect planning which consists of physical hierarchy generation floorplanning with interconnect planning and interconnect architecture planning Chapter 3 investigates a net cut minimization based placement tool Dragon which integrates the state of the art partitioning and placement techniques

Designing Embedded Processors Jörg Henkel, Sri Parameswaran, 2007-07-27 As we embrace the world of personal portable and perplexingly complex digital systems it has befallen upon the bewildered designer to take advantage of the available transistors to produce a system which is small fast cheap and correct yet possesses increased functionality Increasingly these systems have to consume little energy Designers are increasingly turning towards small processors which are low power and customize these processors both in software and hardware to achieve their objectives of a low power system which is verified and has short design turnaround times *Designing Embedded Processors* examines the many ways in which processor based systems are designed to allow low power devices It looks at processor design methods memory optimization dynamic voltage scaling methods compiler methods and multi processor methods Each section has an introductory chapter to give a breadth view and have a few specialist chapters in the area to give a deeper perspective The book provides a good starting point to engineers in the area and to research students embarking upon the exciting area of embedded systems and architectures

Embedded Systems Handbook Richard Zurawski, 2005-08-16 Embedded systems are nearly ubiquitous and books on individual topics or components of embedded systems are equally abundant

Unfortunately for those designers who thirst for knowledge of the big picture of embedded systems there is not a drop to drink Until now *The Embedded Systems Handbook* is an oasis of information offering a mix of basic a

Embedded Systems Handbook 2-Volume Set Richard Zurawski, 2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts

from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems

[The Circuits and Filters Handbook](#) Wai-Kai Chen, 2002-12-23 A bestseller in its first edition The Circuits and Filters Handbook has been thoroughly updated to provide the most current most comprehensive information available in both the classical and emerging fields of circuits and filters both analog and digital This edition contains 29 new chapters with significant additions in the areas of computer

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