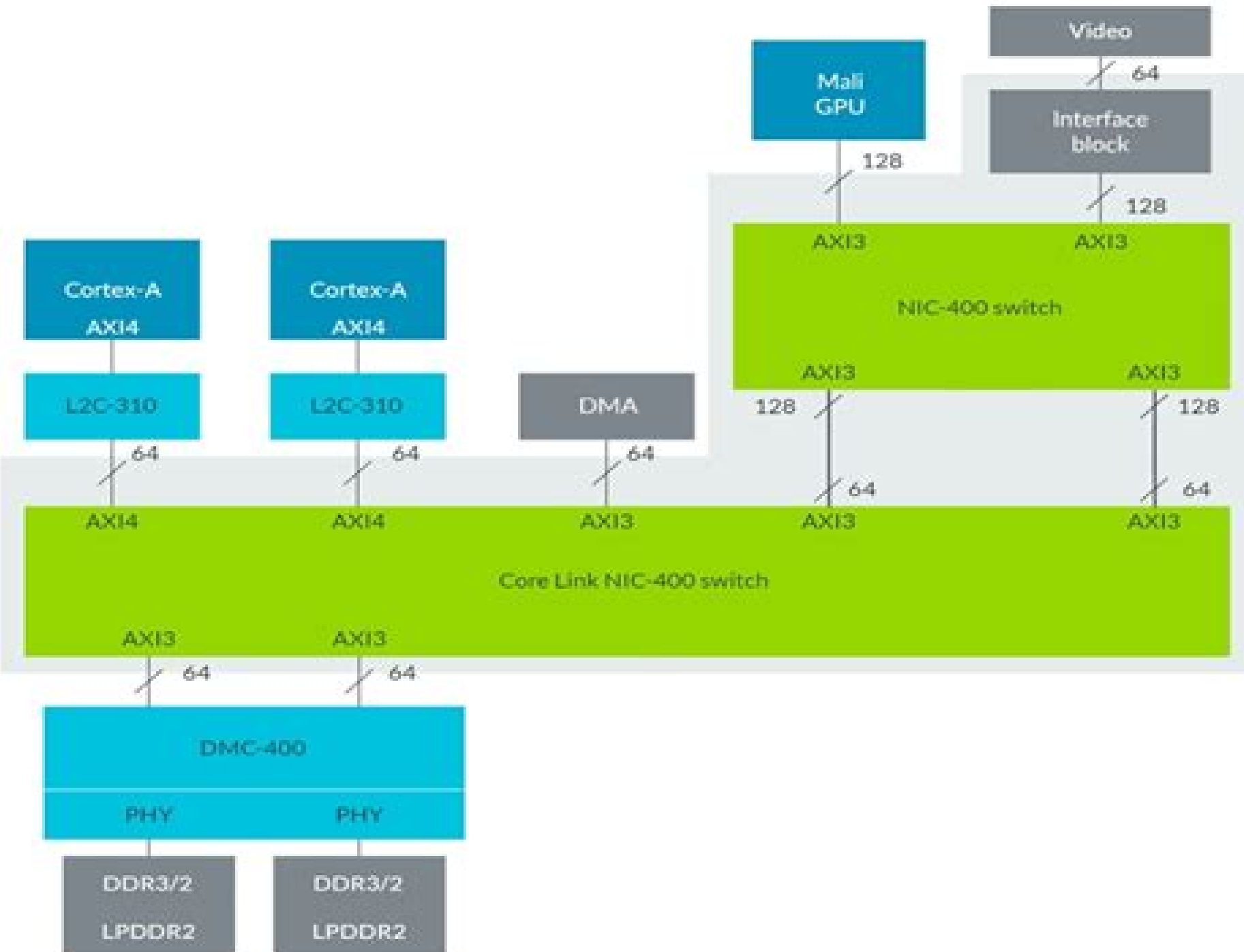




Amba Axi Reference Guide

**K. Ashoka Reddy, B. Rama Devi, Bobby
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Data Engineering and Communication Technology K. Ashoka Reddy, B. Rama Devi, Bobby George, K. Srujan Raju, 2021-05-23 This book includes selected papers presented at the 4th International Conference on Data Engineering and Communication Technology ICDECT 2020 held at Kakatiya Institute of Technology Science Warangal India during 25-26 September 2020. It features advanced multidisciplinary research towards the design of smart computing information systems and electronic systems. It also focuses on various innovation paradigms in system knowledge intelligence and sustainability which can be applied to provide viable solutions to diverse problems related to society, the environment, and industry.

Applied Reconfigurable Computing Vanderlei Bonato, Christos Bouganis, Marek Gorgon, 2016-03-15 This book constitutes the refereed proceedings of the 12th International Symposium on Applied Reconfigurable Computing ARC 2016 held in Rio de Janeiro, Brazil, in March 2016. The 20 full papers presented in this volume were carefully reviewed and selected from 47 submissions. They are organized in topical headings named video and image processing, fault tolerant systems, tools and architectures, signal processing, and multicore systems. In addition, the book contains 3 invited papers and 8 poster papers on funded R&D running and completed projects.

An Open-Source Research Platform for Heterogeneous Systems on Chip Andreas Dominic Kurth, 2022-10-05 Heterogeneous systems on chip (HeSoCs) combine general purpose feature rich multi-core host processors with domain specific programmable many-core accelerators (PMCA) to unite versatility with energy efficiency and peak performance. By virtue of their heterogeneity, HeSoCs hold the promise of increasing performance and energy efficiency compared to homogeneous multiprocessors because applications can be executed on hardware that is designed for them. However, this heterogeneity also increases system complexity substantially. This thesis presents the first research platform for HeSoCs where all components from accelerator cores to application programming interface are available under permissive open source licenses. We begin by identifying the hardware and software components that are required in HeSoCs and by designing a representative hardware and software architecture. We then design, implement, and evaluate four critical HeSoC components that have not been discussed in research at the level required for an open source implementation. First, we present a modular topology agnostic high performance on-chip communication platform which adheres to a state-of-the-art industry standard protocol. We show that the platform can be used to build high bandwidth e.g. 25 GHz and 1024 bit data width end-to-end communication fabrics with high degrees of concurrency e.g. up to 256 independent concurrent transactions. Second, we present a modular and efficient solution for implementing atomic memory operations in highly scalable many-core processors which demonstrates near optimal linear throughput scaling for various synthetic and real-world workloads and requires only 0.5 kGE per core. Third, we present a hardware/software solution for shared virtual memory that avoids the majority of translation lookaside buffer misses with prefetching, supports parallel burst transfers without additional buffers, and can be scaled with the workload and number of parallel processors. Our work

improves accelerator performance for memory intensive kernels by up to 4 Fourth we present a software toolchain for mixed data model heterogeneous compilation and OpenMP offloading Our work enables transparent memory sharing between a 64 bit host processor and a 32 bit accelerator at overheads below 0.7 % compared to 32 bit only execution Finally we combine our contributions to a research platform for state of the art HeSoCs and demonstrate its performance and flexibility

FPGAs Juan Jose Rodriguez Andina, Eduardo de la Torre Aranz, Maria Dolores Valdes, 2017-07-28 Field Programmable Gate Arrays FPGAs are currently recognized as the most suitable platform for the implementation of complex digital systems targeting an increasing number of industrial electronics applications They cover a huge variety of application areas such as aerospace food industry art industrial automation automotive biomedicine process control military logistics power electronics chemistry sensor networks robotics ultrasound security and artificial vision This book first presents the basic architectures of the devices to familiarize the reader with the fundamentals of FPGAs before identifying and discussing new resources that extend the ability of the devices to solve problems in new application domains Design methodologies are discussed and application examples are included for some of these domains e.g. mechatronics robotics and power systems Handbook of Computer Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications

On-Chip Communication Architectures Sudeep Pasricha, Nikil Dutt, 2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures

covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

Rules and Reasoning Paul Fodor, Marco Montali, Diego Calvanese, Dumitru Roman, 2019-09-13 This book constitutes the proceedings of the International Joint Conference on Rules and Reasoning RuleML RR 2019 held in Bolzano Italy during September 2019 This is the third conference of a new series joining the efforts of two existing conference series namely RuleML International Web Rule Symposium and RR Web Reasoning and Rule Systems The 10 full research papers presented together with 5 short technical communications papers were carefully reviewed and selected from 26 submissions

Formal Methods for Industrial Critical Systems Michael Dierkes, Charles Pecheur, 2013-09-16 This book constitutes the thoroughly refereed conference proceedings of the 18th International Workshop on Formal Methods for Industrial Critical Systems FMICS 2013 held in Madrid Spain in September 2013 The 13 papers presented were carefully selected from 25 submissions and cover topics such as design specification code generation and testing based on formal methods methods techniques and tools to support automated analysis certification debugging learning optimization and transformation of complex distributed dependable real time systems and embedded systems verification and validation methods tools for the development of formal design descriptions case studies and experience reports on industrial applications of formal methods impact of the adoption of formal methods on the development process and associated costs application of formal methods in standardization and industrial forums

Tools and Algorithms for the Construction and Analysis of Systems Christel Baier, Cesare Tinelli, 2015-03-30 This book constitutes the proceedings of the 21st International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS 2015 which took place in London UK in April 2015 as part of the European Joint Conferences on Theory and Practice of Software ETAPS 2015 The 45 papers included in this volume consisting of 27 research papers 2 case study papers 7 regular tool papers and 9 tool demonstration papers were carefully reviewed and selected from 164 submissions In addition the book contains one invited contribution The papers have been organized in topical sections on hybrid systems program analysis verification and abstraction tool demonstrations stochastic models SAT and SMT partial order reduction bisimulation and fairness competition on software verification parameter synthesis program synthesis program and runtime verification temporal logic and automata and model checking

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