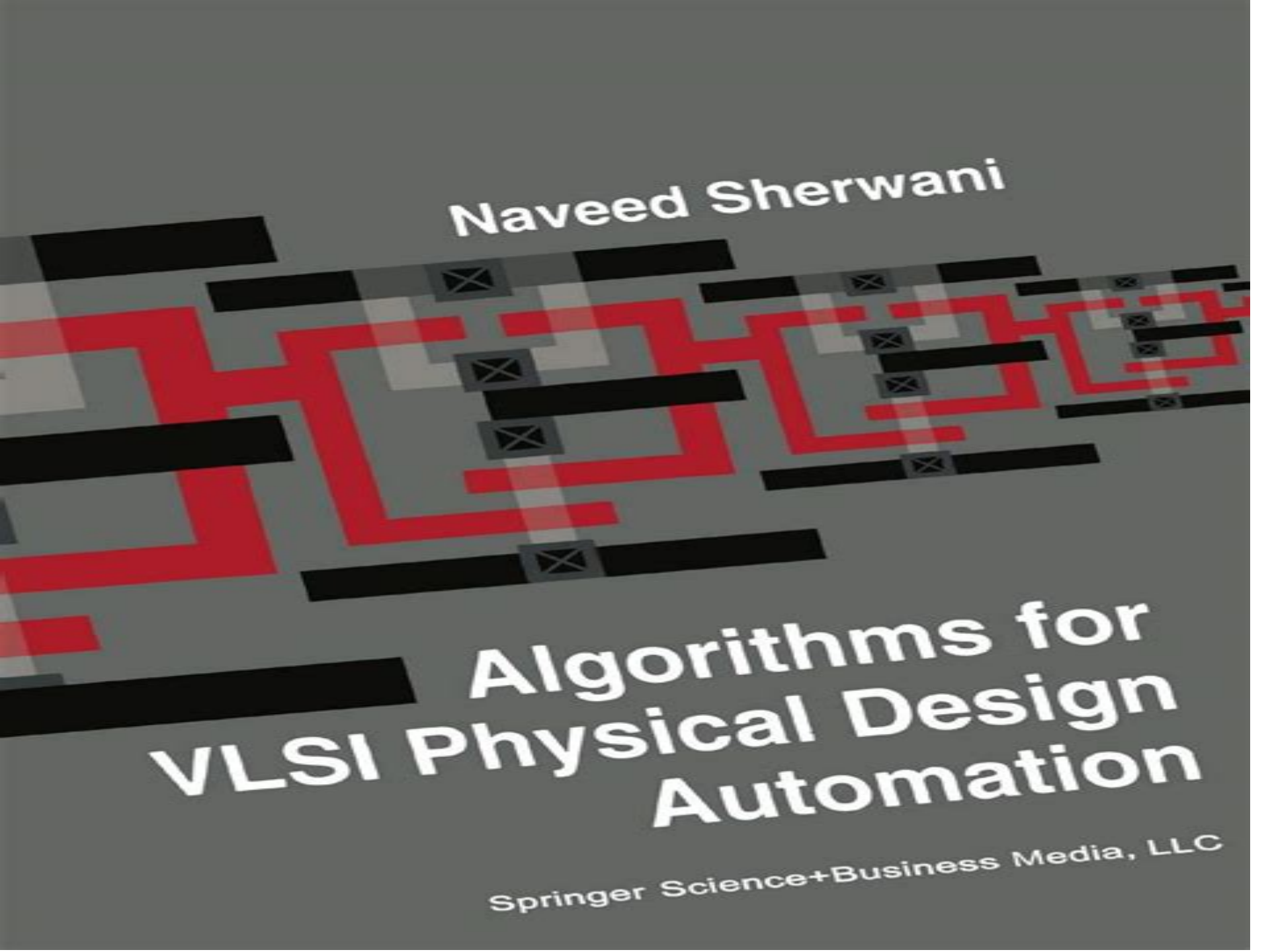




Naveed Sherwani

Algorithms for VLSI Physical Design Automation

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