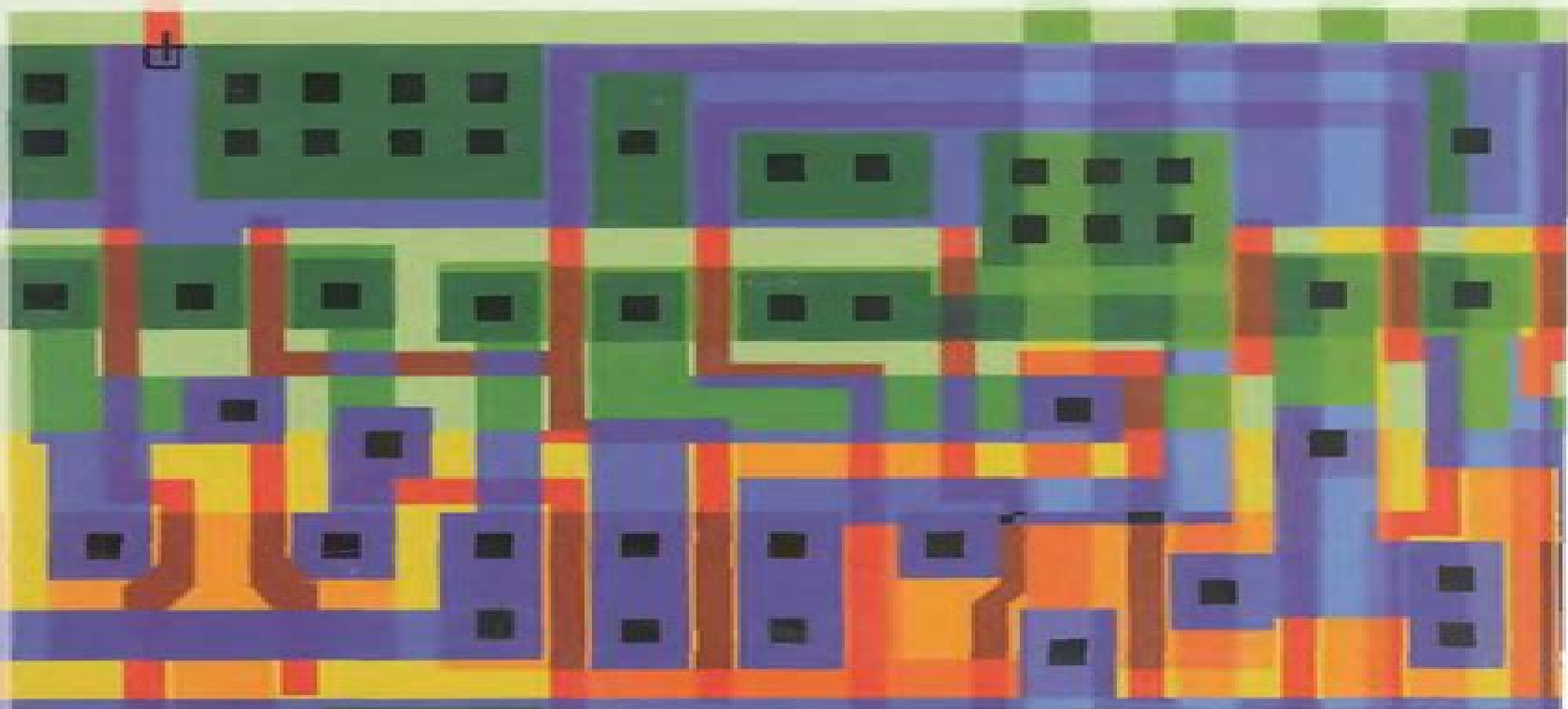


Algorithms for **VLSI** Design Automation



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Analysis & Optimization of Floor Planning Algorithms for VLSI Physical Design Dr. Ashad Ullah

Qureshi,2022-07-01 As prevailing copper interconnect technology advances to its fundamental physical limit interconnect delay due to ever increasing wire resistivity has greatly limited the circuit miniaturization Carbon nanotube CNT interconnects have emerged as promising replacement materials for copper interconnects due to their superior conductivity Buffer insertion for CNT interconnects is capable of improving circuit timing of signal nets with limited buffer deployment However due to the imperfection of fabricating long straight CNT there exist significant unidimensional spatially correlated variations on the critical CNT geometric parameters such as the diameter and density which will act the circuit performance This dissertation develops a novel timing driven buffer insertion technique considering unidimensional correlations of variations of CNT Although the fabrication variations of CNTs are not desired for the circuit designs targeting performance optimization and reliability these inherent imperfections make them natural candidates for building highly secure physical unclonable function PUF which is an advanced hardware security technology A novel CNT PUF design through leveraging Lorenz chaotic system is developed and we show that it is resistant to many machine learning modeling attacks In summary the studies in this dissertation demonstrate that CNT technology is highly promising for performance and security optimizations in advanced VLSI circuit design Handbook of Algorithms for Physical Design Automation Charles J.

Alpert,Dinesh P. Mehta,Sachin S. Sapatnekar,2008-11-12 The physical design flow of any project depends upon the size of the design the technology the number of designers the clock frequency and the time to do the design As technology advances and design styles change physical design flows are constantly reinvented as traditional phases are removed and new ones are added to accommodate changes in technology Handbook of Algorithms for Physical Design Automation provides a detailed overview of VLSI physical design automation emphasizing state of the art techniques trends and improvements that have emerged during the previous decade After a brief introduction to the modern physical design problem basic algorithmic techniques and partitioning the book discusses significant advances in floorplanning representations and describes recent formulations of the floorplanning problem The text also addresses issues of placement net layout and optimization routing multiple signal nets manufacturability physical synthesis special nets and designing for specialized technologies It includes a personal perspective from Ralph Otten as he looks back on the major technical milestones in the history of physical design automation Although several books on this topic are currently available most are either too broad or out of date Alternatively proceedings and journal articles are valuable resources for researchers in this area but the material is widely dispersed in the literature This handbook pulls together a broad variety of perspectives on the most challenging problems in the field and focuses on emerging problems and research results *Proceedings of the First International Scientific Conference "Intelligent Information Technologies for Industry" (IITI'16)* Ajith Abraham,Sergey Kovalev,Valery Tarassov,Václav

Snášel,2016-05-10 This volume of Advances in Intelligent Systems and Computing contains papers presented in the main track of IITI 2016 the First International Conference on Intelligent Information Technologies for Industry held in May 16 21 in Sochi Russia The conference was jointly co organized by Rostov State Transport University Russia and V B Technical University of Ostrava Czech Republic with the participation of Russian Association for Artificial Intelligence RAAI and Russian Association for Fuzzy Systems and Soft Computing RAFSSC The volume is devoted to practical models and industrial applications related to intelligent information systems The conference has been a meeting point for researchers and practitioners to enable the implementation of advanced information technologies into various industries Nevertheless some theoretical talks concerning the state of the art in intelligent systems and soft computing are included in the proceedings as well **Digest of Technical Papers** ,1987 **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology** ,1997 **Artificial Intelligence Perspectives and Applications** Radek Silhavy,Roman

Senkerik,Zuzana Kominkova Oplatkova,Zdenka Prokopova,Petr Silhavy,2015-04-25 This volume is based on the research papers presented in the 4th Computer Science On line Conference The volume Artificial Intelligence Perspectives and Applications presents new approaches and methods to real world problems and in particular exploratory research that describes novel approaches in the field of artificial intelligence Particular emphasis is laid on modern trends in selected fields of interest New algorithms or methods in a variety of fields are also presented The Computer Science On line Conference CSOC 2015 is intended to provide an international forum for discussions on the latest high quality research results in all areas related to Computer Science The addressed topics are the theoretical aspects and applications of Computer Science Artificial Intelligences Cybernetics Automation Control Theory and Software Engineering VLSI-SoC: System-on-Chip in the Nanoscale Era - Design, Verification and Reliability Thomas Hollstein,Jaan Raik,Sergei Kostin,Anton Tšertov,Ian O'Connor,Ricardo Reis,2017-08-31 This book contains extended and revised versions of the best papers presented at the 24th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2016 held in Tallinn Estonia in September 2016 The 11 papers included in the book were carefully reviewed and selected from the 36 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the latest scientific and industrial results and developments as well as future trends in the field of System on Chip SoC Design

Experimental and Efficient Algorithms Klaus Jansen,Marian Margraf,Monaldo Mastrolilli,José D. P. Rolim,2007-12-03 This book constitutes the refereed proceedings of the Second International Workshop on Experimental and Efficient Algorithms WEA 2003 held in Ascona Switzerland in May 2003 The 19 revised full papers presented together with 3 invited contributions were carefully reviewed and selected from 40 submissions The focus of the volume is on applications of efficient algorithms for combinatorial problems Artificial Intelligence Abstracts ,1986 *Channel Coding: Theory, Algorithms, and Applications* ,2014-07-29 This book gives a review of the principles methods and techniques of important and

emerging research topics and technologies in Channel Coding including theory algorithms and applications Edited by leading people in the field who through their reputation have been able to commission experts to write on a particular topic With this reference source you will Quickly grasp a new area of research Understand the underlying principles of a topic and its applications Ascertain how a topic relates to other areas and learn of the research issues yet to be resolved Quick tutorial reviews of important and emerging topics of research in Channel Coding Presents core principles in Channel Coding theory and shows their applications Reference content on core principles technologies algorithms and applications Comprehensive references to journal articles and other literature on which to build further more specific and detailed knowledge

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