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Debashis Dutta, Haranath Kar, Chiranjeev Kumar, Vijaya Bhadauria

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Design and Modeling of Low Power VLSI Systems Sharma, Manoj, Gautam, Ruchi, Khan, Mohammad Ayoub, 2016-06-06 Very Large Scale Integration VLSI Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip Emerging research in this area has the potential to uncover further applications for VSLI technologies in addition to system advancements Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization Through a research based discussion of the technicalities involved in the VLSI hardware development process cycle this book is a useful resource for researchers engineers and graduate level students in computer science and engineering Radio Frequency Identification Stefan Mangard, Patrick Schaumont, 2015-09-24 This book constitutes the thoroughly refereed post conference proceedings of the 11th International Workshop on Radio Frequency Identification held in New York USA in June 2015 The 10 revised full papers were carefully reviewed and selected from 23 submissions and are organized in topical sections on PUFs and applications side channels and countermeasures RFID system attacks and efficient implementations Semiconductor Nanotechnology Stephen M. Goodnick, Anatoli Korkin, Robert Nemanich, 2018-07-26 This book presents research dedicated to solving scientific and technological problems in many areas of electronics photonics and renewable energy Energy and information are interconnected and are essential elements for the development of human society Transmission processing and storage of information requires energy consumption while the efficient use and access to new energy sources requires new information ideas and expertise and the design of novel systems such as photovoltaic devices fuel cells and batteries Semiconductor physics creates the knowledge base for the development of information computers cell phones etc and energy photovoltaic technologies The exchange of ideas and expertise between these two technologies is critical and expands beyond semiconductors Continued progress in information and renewable energy technologies requires miniaturization of devices and reduction of costs energy and material consumption The latest generation of electronic devices is now approaching nanometer scale dimensions new materials are being introduced into electronics manufacturing at an unprecedented rate and alternative technologies to mainstream CMOS are evolving Nanotechnology is widely accepted as a source of potential solutions in securing future progress for information and energy technologies Semiconductor Nanotechnology features chapters that cover the following areas atomic scale materials design bio and molecular electronics high frequency electronics fabrication of nanodevices magnetic materials and spintronics materials and processes for integrated and subwave optoelectronics nanoCMOS new materials for FETs and other devices nanoelectronics system architecture nano optics and lasers non silicon materials and devices chemical and biosensors quantum effects in devices nano science and technology applications in the development of novel solar energy devices and fuel cells and batteries **VLSI-SoC: Design** 

for Reliability, Security, and Low Power Youngsoo Shin, Chi Ying Tsui, Jae-Joon Kim, Kiyoung Choi, Ricardo Reis, 2016-09-12 This book contains extended and revised versions of the best papers presented at the 23rd IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2015 held in Daejeon Korea in October 2015 The 10 papers included in the book were carefully reviewed and selected from the 44 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about new challenges both at the physical and system design levels as well as in the test of these systems Hybrid ADCs, Smart Sensors for the IoT, and Sub-1V & Advanced Node Analog Circuit Design Pieter Harpe, Kofi A. A. Makinwa, Andrea Baschirotto, 2017-09-18 This book is based on the 18 tutorials presented during the 26th workshop on Advances in Analog Circuit Design Expert designers present readers with information about a variety of topics at the frontier of analog circuit design with specific contributions focusing on hybrid ADCs smart sensors for the IoT sub 1V and advanced node analog circuit design This book serves as a valuable reference to the state of the art for anyone involved in analog circuit research and development Advances in VLSI, Communication, and Signal Processing Debashis Dutta, Haranath Kar, Chiranjeev Kumar, Vijaya Bhadauria, 2019-12-03 This book comprises select proceedings of the International Conference on VLSI Communication and Signal processing VCAS 2018 It looks at latest research findings in VLSI design and applications The book covers a wide range of topics in electronics and communication engineering especially in the area of microelectronics and VLSI design communication systems and networks and image and signal processing The contents of this book will be useful to researchers and professionals alike

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