

3. Balanced Modulator

Aim:

1. To construct and properly adjust a balanced modulator and study its operation.
2. To observe that the output is a double sideband with a suppressed carrier signal.
3. To adjust it for optimum carrier suppression.
4. To verify that the output audio level directly affects the double side-band output amplitude.
5. To observe that the output is maximum with zero audio input.
6. To measure carrier only output and the peak side-band output and to calculate the carrier suppression.

Equipment Required:

Analog communication kit, CRO connecting wires.

Component Required:

1. IC'S: 1496- (1no)
2. Resistors: 100Ω -(1no), 820Ω -(1no), $1k\Omega$ -(1no), $1.2k\Omega$ -(1no), $2.7k\Omega$ -(1no), $10k\Omega$ -(3nos), $47k\Omega$ -(1no), $50k\Omega$ -POT
3. Capacitors: $1\mu F$ -(1no), $22\mu F$ -(1no), $100\mu F$ -(2nos)

Theory:

One circuit that lends itself extremely well to balanced modulator application is the differential amplifier. A simplified diagram of a differential amplifier is shown in figure 1.



FIG 1: A Differential amplifier used as a balanced modulator

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