

III Semester B.E. (E&E) Examination, Dec. 2018/Jan. 2019
(2K11 Scheme)

EE 301 : ANALOG ELECTRONIC CIRCUITS

Time : 3 Hours

Max. Marks : 100

Instruction : Answer any five full questions, by choosing atleast two from each Part.

PART – A

1. a) Classify the diode clipping circuits with neat circuits and relevant waveforms. 8
- b) For the circuit shown below figure 1. b) sketch the transfer characteristic and o/p waveform (Assume ideal diodes). 6

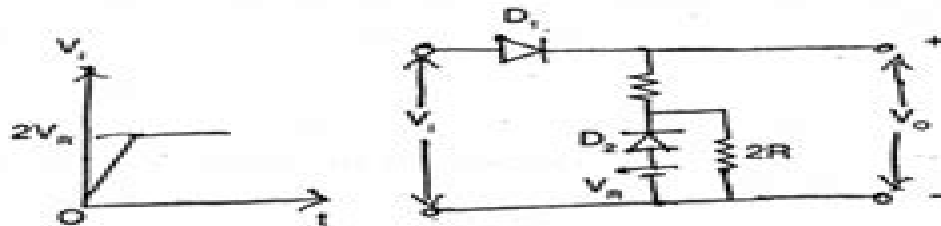


Fig. 1(b)

- c) Define clamping circuit and also explain the working of positive peak clamper. 6
2. a) Define Q-point. Discuss the issues related to stability of operating point of BJT amplifiers. 6
- b) Compare and contrast stability factor of fixed-bias and self-biased BJT circuits. 6
- c) Design a voltage divider bias circuit using a silicon transistor with $V_{CC} = 18V$, $I_C = 2.3 \text{ mA}$, $V_{CE} = 8.2 \text{ V}$, $R_C = 3.3 \text{ K}$, $\beta = 100$ and $S(I_{\infty}) \leq 5$. 8

P.T.O.

Analog Electronic Circuit 3rd Sem Ec

L Darling-Hammond



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Analog Electronic Circuits (For 3rd Semester of APJKTU, Kerala) Sukumaran P., Analog Electronic Circuits

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electronics. Even in the era of digital dominance, analog circuits remain indispensable; they bridge the gap between the real world of continuous signals and the digital world of computation. This book, *Analog Electronic Circuits*, has been crafted to provide students with a clear and comprehensive understanding of the analog circuits. The motivation behind this book stems from the need for a balanced resource that not only explains the theoretical foundations of analog electronics but also emphasizes practical implementation, analysis, and troubleshooting. Topics such as amplifiers, feedback oscillators, and power devices have been covered systematically. Special care has been taken to make the text accessible to undergraduate students. Each chapter builds on the previous, ensuring a gradual yet thorough progression from basic to more complex concepts.

Analog Electronics Balwinder Singh, Ashish Dixit, 2007 Electric Circuits And Networks (For Gtu) Kumar K. S.

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