



GRADE 10 MATHEMATICAL LITERACY JUNE EXAM PAPER 2

QUESTION 3 10 MARKS

Jane is going on a hockey tour. The hockey girls decide to host a movie night at school to raise funds. Entrance to the movie is R20,00 per person or R25,00 including popcorn and a cold drink. 80 learners pay entrance only and 125 learners pay for the combo.

- 3.1 Write the number of learners that paid R20 versus the number of learners that paid R25 as a ratio in its simplest form. (2)
- 3.2 The school has 325. Write the number of learners that will be attending the movie night as a fraction over the total amount of learners in the school. (2)
- 3.3 Determine the percentage of learners that will be attending the movie night. (3)
- 3.4 Determine the gross income of the movie night. (3)

QUESTION 2 MAPS & PLANS 10 MARKS

Use the information below to answer the questions that follow.

The movie night will take place in the school hall. The dimensions of the hall is 50m x 15m. Adjacent to the left of the hall are boys and girls' bathrooms with combined dimensions of 10m x 5m.

- 2.1 Draw a floor plan of the school hall and the bathrooms. (5)
- 2.2 How many learners can fit into the hall if they allocate 2 learners per 1m²? (3)
- 2.3 Based on your answer in QUESTION 2.2, will there be enough space for the hockey girls attending the movie night? (2)

QUESTION 3 MEASUREMENT 10 MARKS

Jane wants her team to do practice drills to improve their fitness. Practice will be for 100 minutes starting at 14h15.

- 3.1 Convert 100 minutes to hours. (2)
- 3.2 Draw a line graph showing the time taken for the practice drills. (2)
- The dimensions of the hockey field are 91m x 55m.
- 3.3 Draw a line graph showing the time taken for the practice drills. (2)
- 3.4 Convert your answer in QUESTION 3.3 to kilometres. (2)
- The hockey girls practice 3 times a week for the whole season that is roughly around 15 weeks.
- 3.5 Draw a line graph showing the time taken for the practice drills. (2)

QUESTION 4 10 MARKS

The layout of the school where the hockey tour is taking place is shown on ANNEXURE A. Study the map to answer the following questions.

- 4.1 Kyle is late and you need to give her directions from the main gate to the hockey field. (2)
- 4.2 Draw a line graph showing the time taken for the practice drills. (2)
- Use the formula:
 $\text{Time} = \text{Distance} \div \text{Speed}$
- 4.3 In which direction is the tuck shop from the C pavilion? (2)
- 4.4 If they girls stay at a guest house 15min away from the school, what time should they leave the school to get to the guest house? (3)
- 4.5 Draw a line graph showing the time taken for the practice drills. (2)
- that time? why/why not?

Answers Grade10 Math Literacy Paper 2 June

Léo Pomar



Answers Grade10 Math Literacy Paper 2 June:

Resources in Education ,2001-10 **The Role of Letter-Speech Sound Integration in Typical and Atypical Reading Development** Jurgen Tijms,Silvia Brem,Gorka Fraga González,Iliana I. Karipidis,2020-07-24 Fluency is the quintessence of effective reading To obtain socio economic success fluent reading is of primordial importance and reading is considered a crucial marker of an individual s life course Approximately 5% of children are affected by developmental dyslexia exhibiting inaccurate word recognition spelling phonological decoding and most importantly severely dysfluent reading which remains as their most characterizing and persistent deficit Unable to attain society s literacy demands individuals with dyslexia are at severe risk for adverse academic economic and psychosocial consequences Recently it has been posed that the development of automatic letter speech sound LSS integration is critical in the acquisition of fluent reading skills and in particular that a failure to develop automatic LSS integration results in an impairment of reading fluency In support neurocognitive research has suggested that the development of automatized processing of LSS associations is an essential step in the formation of a functional neural network for reading Furthermore both neurocognitive and behavioural studies have suggested a less efficient LSS integration in children with dyslexia than in typical readers Finally results from intervention studies have suggested that training LSS might be a promising approach to ameliorate dysfluent reading in children with dyslexia Nonetheless there is still a considerable gap of knowledge in our understanding of the mechanisms by which learning LSS associations relate to dys fluent reading **Resources in Women's Educational Equity** ,1977 Literature cited in AGRICOLA Dissertations abstracts international ERIC ABI INFORM MEDLARS NTIS Psychological abstracts and Sociological abstracts Selection focuses on education legal aspects career aspects sex differences lifestyle and health Common format bibliographical information descriptors and abstracts and ERIC subject terms used throughout Contains order information Subject author indexes **Resources in Education** ,1996 American Education ,1983 Canadian Books in Print ,2000 Effective Grading Practices for Secondary Teachers Dave Nagel,2015-03-04 Enact innovative grading systems that more accurately describe student progress This book challenges traditional grading practices and provides alternatives that can have direct impact on student success By making subtle shifts toward standards based grading systems schools can reduce unnecessary course failures provide students and their families a more accurate picture of current progress and increase opportunities for success The author offers a range of grading reform strategies that are built from practical frameworks that are effective and simple to adapt Among the many strengths of this book are Practical application of existing research and evidence base for effective secondary grading reforms A framework for schools and districts to apply and adapt failure prevention strategies such as early failure detection Amnesty Days and meaningful stipulated second chance opportunities for students to reach mastery Functional strategies and actions for shifting toward standards based referenced grading without entirely abandoning letter grades Countering

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Failure Vicki E. Alger,2016-07-01 A comprehensive account and frank assessment of federal involvement in education is long overdue Education policy expert Vicki E Alger remedies this deficiency with her book *Failure The Federal Miseducation of America s Children* As its title indicates *Failure* makes no effort to sugar coat its findings Created in 1979 after a lobbying campaign that spanned generations the Department of Education has failed to live up to its promises Federal involvement whether related to testing funding or academic curricula has failed to abide by the Constitution s implication that education must remain the domain only of state and local governments and private institutions Most of all the central government s pervasive meddling in education has failed America s school children and their parents Education policy has long been mired in controversies often with opposing sides missing the mark *Failure* helps us step back from the skirmish du jour and redirects our focus to the big picture showing us what s gone wrong over the decades and the institutional causes of these failures It also offers a bold blueprint for returning the federal government to its constitutional role and for cultivating an educational system that meets the needs of students and parents rather than bureaucrats Concerned citizens of every stripe will benefit from *Failure* s history of federal education policy its brutally honest report card for the Department of Education its look at education systems across the globe and its ambitious policy recommendations *Failure* might even succeed in reframing the way the federal education establishment thinks about education policy

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Table of Contents Answers Grade10 Math Literacy Paper 2 June

1. Understanding the eBook Answers Grade10 Math Literacy Paper 2 June
 - The Rise of Digital Reading Answers Grade10 Math Literacy Paper 2 June
 - Advantages of eBooks Over Traditional Books
2. Identifying Answers Grade10 Math Literacy Paper 2 June
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Answers Grade10 Math Literacy Paper 2 June
 - User-Friendly Interface

4. Exploring eBook Recommendations from Answers Grade10 Math Literacy Paper 2 June
 - Personalized Recommendations
 - Answers Grade10 Math Literacy Paper 2 June User Reviews and Ratings
 - Answers Grade10 Math Literacy Paper 2 June and Bestseller Lists
5. Accessing Answers Grade10 Math Literacy Paper 2 June Free and Paid eBooks
 - Answers Grade10 Math Literacy Paper 2 June Public Domain eBooks
 - Answers Grade10 Math Literacy Paper 2 June eBook Subscription Services
 - Answers Grade10 Math Literacy Paper 2 June Budget-Friendly Options
6. Navigating Answers Grade10 Math Literacy Paper 2 June eBook Formats
 - ePub, PDF, MOBI, and More
 - Answers Grade10 Math Literacy Paper 2 June Compatibility with Devices
 - Answers Grade10 Math Literacy Paper 2 June Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Answers Grade10 Math Literacy Paper 2 June
 - Highlighting and Note-Taking Answers Grade10 Math Literacy Paper 2 June
 - Interactive Elements Answers Grade10 Math Literacy Paper 2 June
8. Staying Engaged with Answers Grade10 Math Literacy Paper 2 June
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Answers Grade10 Math Literacy Paper 2 June
9. Balancing eBooks and Physical Books Answers Grade10 Math Literacy Paper 2 June
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Answers Grade10 Math Literacy Paper 2 June
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Answers Grade10 Math Literacy Paper 2 June
 - Setting Reading Goals Answers Grade10 Math Literacy Paper 2 June
 - Carving Out Dedicated Reading Time

12. Sourcing Reliable Information of Answers Grade10 Math Literacy Paper 2 June
 - Fact-Checking eBook Content of Answers Grade10 Math Literacy Paper 2 June
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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web nov 8 2018 in this post we will take a look at implementing the vhdl code for all logic gates using dataflow architecture first we will take a look at the logic equations of all the gates and then the syntax we will also test our logic by writing a testbench

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web from wikipedia the free encyclopedia vhdl source for a signed vhsic hardware description languagevhdl is a hardware description language hdl that can model the behavior and structure of at multiple levels of abstraction ranging from the system level down to that of logic gates for design entry documentation and verification purposes

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web oct 22 2023 alu project overview in this project i will walk through the creation of the 8 bit alu circuit with input dip switches and output leds demonstrated in operation in figure 1 i used the vhdl language to code the alu and ran it on a

complex programmable logic device cpld development board

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web apr 7 2014 the vhdl code for full adder circuit adds three one bit binary numbers a b cin and outputs two one bit binary numbers a sum s and a carry cout truth table describes the functionality of full adder sum s output is high when odd number of inputs are high cout is high when two or more inputs are high

and all elements of an n bit array in vhdl stack overflow - Apr 14 2022

web nov 30 2013 lets say i have an n bit array i want to and all elements in the array similar to wiring each element to an n bit and gate how do i achieve this in vhdl note i am trying to use reusable vhdl code so i want to avoid hard coding something like result array 0 and array 1 and array 2 and array n thanks oshara

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web this tutorial describes language features that are common to all versions of the language they are expressed using the syntax of vhdl 93 and subsequent versions there are some aspects of syntax that are incompatible with the original vhdl 87 version however most tools now support at least vhdl 93 so syntactic differences

using the work library in vhdl xilinx support - Sep 19 2022

web feb 16 2023 by using the coding style above there is no question about which version of bottom is wanted the version in the library my lib1 is the correct one care should be taken when choosing library names for vhdl files while a library called work is a common library name for many projects the tool will handle it a little differently than

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web high level synthesisable vhdl hvhdl is a set of coding patterns for standard vhdl which are designed to greatly increase abstraction level of vhdl source code using standard synthesis tools the coding patterns are designed to support incremental design testing and development of the vhdl source code

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web overview visual studio code is an open source editor from microsoft vhdl tool makes its services available to vs code through the language server protocol vscode plugin find the plugin by searching for vhdl in the extensions sidebar or go to the online marketplace follow the instructions in the readme

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web dec 29 2017 vhdl uses a similar description however it has its own syntax for example it uses the following lines of code

to describe the input and output ports of this circuit 1 entity circuit 1 is 2 port a in std logic 3 b in std logic 4 out1 out std logic 5 end circuit 1

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web apr 29 2016 vhdl was created in the 1980s to develop a high speed integrated circuit it has since become an industry standard language for describing digital systems at the structural or behavioral levels in a text based format the vhdl file can be used to manufacture a physical circuit such as a motherboard processor or memory module

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

lab 6 single clock data path for 16 bit r type instructions in - Jun 16 2022

web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let s define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design