FINAL EXAMINATION COE 117 - LOGIC CIRCUIT 1 2rd Qtr. / 2010 - 2011

1. An expression is said to be in this form when all products are the products of only single variables. PERS er. Nelkindserren. d. Maxteem. 594.345 The Boolean expression X(X + Y) = X is an example of which Law/Theorem? a. Simplification De Morgan's Albacorption Commutative 3. The signal in the most present day electronic digital systems use just two discrete values and are therefore said to be in a. Digital c. Analog. Decimal d. Binary The Boolean expression X + X' = 1 is an example of which Law/Theorem? a. Complementary e. Idempotent b. Absorption d. Inequality It is the process by which certain data or information is represented in a numerical pattern. a. Digital System c. Number System Analog System d. None DEAD FACE base 16 = a. 110111101010101101.111110101100101010 110111101010101101.111111000110011102 110411101010101101.10011010410011102 d. 110111101010101101.1111101011001110; The given: 10110110; is equivalent to m. 182₁₀ c. 183₁₀ b. 183.... d. 180_{ab} S. BABE₁₈ + 1010011101₂ a. A978 to BUDGE at FERRO The given: 16_{n0} is equivalent to a. 0100 0011pos b. 0004 0001; c. 00011000pc 48. All of the above 10. The Boolean expression X + X = X is an example of which Law/Theorem? a. Complementary c. Idempetent Absorption d. Inequality 11. This part of the design of digital system involves determining how to interconnect basic logic building blocks to perform a specific function. a. System Design c. Switching Design b. Logic Design d. None. 12. Convert the decimal number 888 to binary c. 1101001000. a. 11011111000₂ b. 1101011000₂ d. 1001111000-13. 0.625 base 10 m basic 16: c. 0.5 a. 0.51 b. 50 dl. 255 14. What is the equivalent gate of two 1-input NOR gates connected in series? ABNID b. Inverter b. Blackber e. Nome 15. It is a form in which Boolean expression is expressed as sum of maxterms or product of mainterns. a. Standard Form
 b. Canonical Form Boolean Expression d. None of the above 16. What is the code use to label the cell of K-map? a. Bimary code h. Gray code. IBCTD: 400 d. Excess-3 code

 What will happen if the function v(w+x+y)x would be implemented using the NOR gate? Six NOR gates will be used. b. The given function must be implemented using OR and NAND gates Not possible using NOR gates None of the above 18. Use K-map to find the minimum sum of products expression for the following functions: (A.B.C.D.E) -5 (1.3.5.7.8.12.15.18.19.72.23.24.27.28.31) ABE + CD'E + AB'D+ADE+BD'E' b. A'B'E + CDE + AB'D+ADE+BD'E' ABE + CD'E + AB D+A DE+BD'E' A'B'E + CD'E + AB'D+ADE+BD'E' 19. Use the K-map to find minimum sum of products of $F(A,B,C,D) = \prod (0,2,4,6,8,10,12,14)$ a. F = DCF-AD b F - D* d. F = ABCD20. Simplify the following Boolean Expression using Boolean Algebra: (wx + yz)(w'y'+xz') in w'x'y' Co. W. DOM in were 'm di we'nce Oiven the function F(a,b,c,d) = ∏(1,3,5,9,12,14,15). determine its canonical form. a. $F = M_1M_2M_4M_4M_{12}M_{12}M_{13}$ $F = m_1 + m_2 + m_3 + m_4 + m_{12} + m_{14} + m_{15}$ $F = m_1 m_2 m_3 m_4 m_{12} m_{14} m_{15}$ $F = M_1 + M_2 + M_3 + M_4 + M_{12} + M_{14} + M_{15}$ 22. A multiplexer is also called _ a. Comparator By. Data Controller C. Duta Interpreter d. Data Selector 23. The 74LS83 is an example of a 4-bit parallel adder. To expand this device to an 8-bit adder you must Use 2 adders and connect the sum outputs of one to the bit inputs of the other Use 8 adders with no interconnections Use two adders with the carry output of the one connected to the carry input of the other d. Use four adders with no interconnections 24. There are _ 1-to-2 decoder to implement a 5to-32 decoder using four 3-to-8 decoder 120 Dec 31 ec. 4 d. NONE 25. How many 3-to-8 decoder is needed to implement a 6-to-64 Decoder? a. 8 Ba. 198 c. 10 26. How many inputs, outputs and selectors are needed to implement 16X1 MUX? a. 8 Inputs, 16 outputs, 3 selectors b. 16 Inputs, 3outputs, 8 selectors 16 outputs, 16 inputs, 4 selectors I output, 16 inputs, 4 selectors 27. Simplify the given Boolean function. $F(A,B,C,D) = \Sigma m(0,6,8,13,14),$ $d(A,B,C,D) = \Sigma m(2,4,10)$ F = C, D, + BD, + V, BCD E = B, D, + V, B, C, D + CDc. F = ABC'D + BD + CDd. F = CD' + B' D' + ABC' D

2011 Tesccc Answer Key

Judd E. Hollander



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