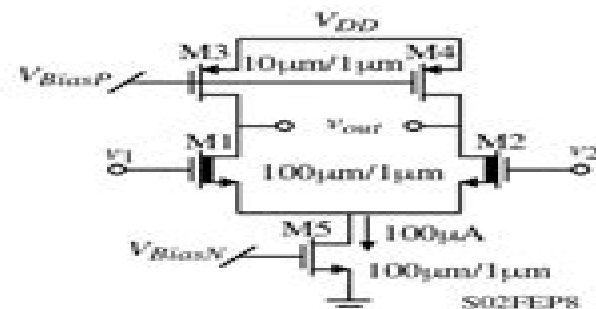


Homework Assignment No. 13Due Friday, April 18, 2003 in classProblem 1 - (10 points)

A differential CMOS amplifier using depletion mode input devices is shown. Assume that the normal MOSFETs parameters are $K_N' = 110\text{V}/\mu\text{A}^2$, $V_{TN} = 0.7\text{V}$, $\lambda_N = 0.04\text{V}^{-1}$ and for the PMOS transistors are $K_P' = 50\text{V}/\mu\text{A}^2$, $V_{TP} = -0.7\text{V}$, $\lambda_P = 0.05\text{V}^{-1}$. For the depletion mode NMOS transistors, the parameters are the same as the normal NMOS except that $V_{TN} = -0.5\text{V}$. (a.) What is the maximum input common-mode voltage, $V_{icm}^*(\text{max})$? (b.) What is the minimum input common-mode voltage, $V_{icm}^*(\text{min})$? (c.) What value of V_{DD} gives an $ICMR = 0.5V_{DD}$?

Problem 2 - (10 points)

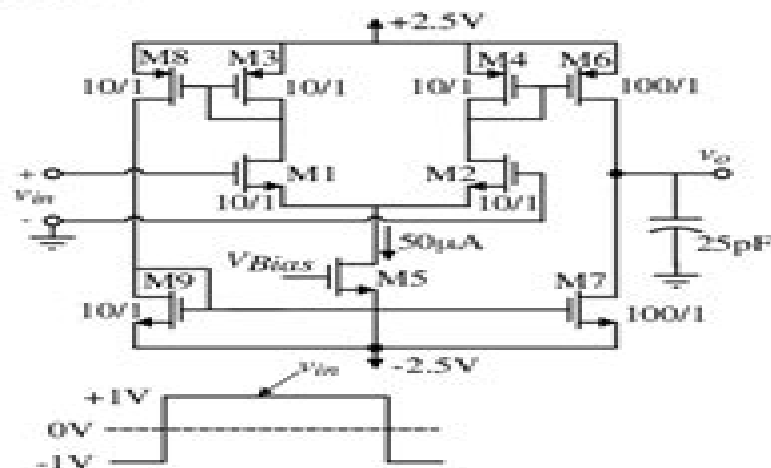
Problem 8.2-2 of Allen and Holberg, 2nd edition

Problem 3 - (10 points)

Problem 8.2-8 of Allen and Holberg, 2nd edition.

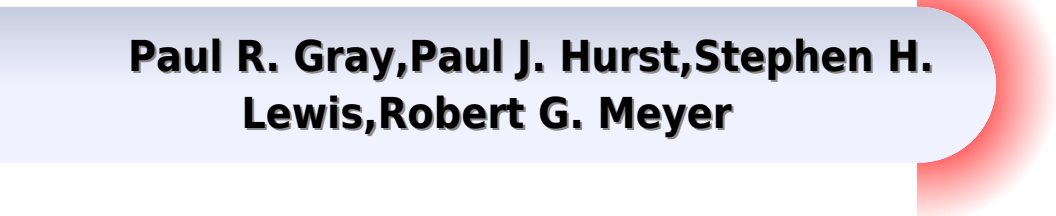
Problem 4 - (10 points)

The comparator shown has an input applied as shown. Assuming the the pulse width is wide enough, calculate the propagation delay time for this comparator. Assume that the trip point of the output is at 0V .



Analog Integrated Circuit Design Problem Answers

**Paul R. Gray, Paul J. Hurst, Stephen H.
Lewis, Robert G. Meyer**



Analog Integrated Circuit Design Problem Answers:

CMOS Analog Circuit Design Holberg Allen, Phillip E. Allen, Douglas R. Holberg, 1995-06 After years of anticipation respected authors Phil Allen and Doug Holberg bring you the second edition of their popular textbook CMOS Analog Circuit Design From the forefront of CMOS technology Phil and Doug have combined their expertise as engineers and academics to present a cutting edge and effective overview of the principles and techniques for designing circuits Their two main goals are DT to mix the academic and practical viewpoints in a treatment that is neither superficial nor overly detailed andDT to teach analog integrated circuit design with a hierarchically organized approach Most of the techniques and principles presented in the second edition have been taught over the last ten years to industry members Their needs and questions have greatly shaped the revision process making this new edition a valuable resource for practicing engineers The trademark approach of Phil and Doug s textbook is its design recipes which take readers step by step through the creation of real circuits explaining complex design problems The book provides detailed coverage of often neglected areas and deliberately leaves out bipolar analog circuits since CMOS is the dominant technology for analog integrated circuit design Appropriate for advanced undergraduates and graduate students with background knowledge in basic electronics including biasing modeling circuit analysis and frequency response CMOS Analog Circuit Design Second Edition presents a complete picture of design including modeling simulation and testing and enables readers to design an analog circuit that can be implemented by CMOS technology FeaturesDT Orients the experience of the expert within the perspective of design methodologyDT Identifies common mistakes made by beginning designersDT Provides problems with each chapter that reinforce and develop student understandingDT Contains numerous problems that can be used as homework quiz or exam problemsDT Includes a new section on switched capacitor circuitsDT Includes helpful appendices that provide simulation techniques and the following supplemental material A brief review of circuit analysis for CMOS analog designA calculator program for analyzing CMOS circuitsA summary of time frequency domain relationships for second order systems *400+ Integrated Circuit Designer Interview Questions And Answers Guide (expert Edition) - Complete Study & Preparation Ebook* CloudRoar Consulting services, 101-01-01 Prepare for the Zscaler Certified Administrator exam with 350 questions and answers covering cloud security firewall policies access control traffic inspection logging and best practices Each question provides practical examples and detailed explanations to ensure exam readiness Ideal for cloud security engineers and administrators Zscaler Certified Administrator Cloud Security Firewall Policies Access Control Traffic Inspection Logging Best Practices Exam Preparation Career Growth Professional Development IT Security Cloud Engineering IT Skills IT Certifications **Parallel Solution of Integral Equation-Based EM Problems in the Frequency Domain** Y. Zhang, T. K. Sarkar, 2009-06-29 A step by step guide to parallelizing cem codes The future of computational electromagnetics is changing drastically as the new generation of computer chips evolves from single core to multi core The burden now falls on software programmers to

revamp existing codes and add new functionality to enable computational codes to run efficiently on this new generation of multi core CPUs In this book you ll learn everything you need to know to deal with multi core advances in chip design by employing highly efficient parallel electromagnetic code Focusing only on the Method of Moments MoM the book covers In Core and Out of Core LU Factorization for Solving a Matrix Equation A Parallel MoM Code Using RWG Basis Functions and ScaLAPACK Based In Core and Out of Core Solvers A Parallel MoM Code Using Higher Order Basis Functions and ScaLAPACK Based In Core and Out of Core Solvers Turning the Performance of a Parallel Integral Equation Solver Refinement of the Solution Using the Conjugate Gradient Method A Parallel MoM Code Using Higher Order Basis Functions and Plapack Based In Core and Out of Core Solvers Applications of the Parallel Frequency Domain Integral Equation Solver Appendices are provided with detailed information on the various computer platforms used for computation a demo shows you how to compile ScaLAPACK and PLAPACK on the Windows operating system and a demo parallel source code is available to solve the 2D electromagnetic scattering problems Parallel Solution of Integral Equation Based EM Problems in the Frequency Domain is indispensable reading for computational code designers computational electromagnetics researchers graduate students and anyone working with CEM software

Nanoelectronic Coupled Problems Solutions E. Jan W. ter Maten,Hans-Georg Brachtendorf,Roland Pulch,Wim Schoenmaker,Herbert De Gersem,2019-11-06 Designs in nanoelectronics often lead to challenging simulation problems and include strong feedback couplings Industry demands provisions for variability in order to guarantee quality and yield It also requires the incorporation of higher abstraction levels to allow for system simulation in order to shorten the design cycles while at the same time preserving accuracy The methods developed here promote a methodology for circuit and system level modelling and simulation based on best practice rules which are used to deal with coupled electromagnetic field circuit heat problems as well as coupled electro thermal stress problems that emerge in nanoelectronic designs This book covers 1 advanced monolithic multirate co simulation techniques which are combined with envelope wavelet approaches to create efficient and robust simulation techniques for strongly coupled systems that exploit the different dynamics of sub systems within multiphysics problems and which allow designers to predict reliability and ageing 2 new generalized techniques in Uncertainty Quantification UQ for coupled problems to include a variability capability such that robust design and optimization worst case analysis and yield estimation with tiny failure probabilities are possible including large deviations like 6 sigma 3 enhanced sparse parametric Model Order Reduction techniques with a posteriori error estimation for coupled problems and for UQ to reduce the complexity of the sub systems while ensuring that the operational and coupling parameters can still be varied and that the reduced models offer higher abstraction levels that can be efficiently simulated All the new algorithms produced were implemented transferred and tested by the EDA vendor MAGWEL Validation was conducted on industrial designs provided by end users from the semiconductor industry who shared their feedback contributed to the measurements and supplied both material data and process data In

closing a thorough comparison to measurements on real devices was made in order to demonstrate the algorithms industrial applicability

Instructor's Solutions Manual for CMOS Analog Circuit Design Phillip Allen, Douglas Holberg, 2011-08 This is a core textbook for a full course on the design and function of Analog Integrated Circuits

Big Data and Visual Analytics Sang C. Suh, Thomas Anthony, 2018-01-15 This book provides users with cutting edge methods and technologies in the area of big data and visual analytics as well as an insight to the big data and data analytics research conducted by world renowned researchers in this field The authors present comprehensive educational resources on big data and visual analytics covering state of the art techniques on data analytics data and information visualization and visual analytics Each chapter covers specific topics related to big data and data analytics as virtual data machine security of big data big data applications high performance computing cluster and big data implementation techniques Every chapter includes a description of an unique contribution to the area of big data and visual analytics This book is a valuable resource for researchers and professionals working in the area of big data data analytics and information visualization Advanced level students studying computer science will also find this book helpful as a secondary textbook or reference

Neonatal Monitoring Technologies: Design for Integrated Solutions Chen, Wei, Oetomo, Sidarto Bambang, Feijs, Loe, 2012-04-30 This book presents a unique integration of knowledge from multidisciplinary fields of engineering industrial design and medical science for the healthcare of a specific user group Provided by publisher

Application of Evolutionary Algorithms for Multi-objective Optimization in VLSI and Embedded Systems M.C. Bhuvaneswari, 2014-08-20 This book describes how evolutionary algorithms EA including genetic algorithms GA and particle swarm optimization PSO can be utilized for solving multi objective optimization problems in the area of embedded and VLSI system design Many complex engineering optimization problems can be modelled as multi objective formulations This book provides an introduction to multi objective optimization using meta heuristic algorithms GA and PSO and how they can be applied to problems like hardware software partitioning in embedded systems circuit partitioning in VLSI design of operational amplifiers in analog VLSI design space exploration in high level synthesis delay fault testing in VLSI testing and scheduling in heterogeneous distributed systems It is shown how in each case the various aspects of the EA namely its representation and operators like crossover mutation etc can be separately formulated to solve these problems This book is intended for design engineers and researchers in the field of VLSI and embedded system design The book introduces the multi objective GA and PSO in a simple and easily understandable way that will appeal to introductory readers

[European Success Stories in Industrial Mathematics](#) Thibaut Lery, Mario Primicerio, Maria J. Esteban, Magnus Fontes, Yvon Maday, Volker Mehrmann, Gonçalo Quadros, Wil Schilders, Andreas Schuppert, Heather Tewkesbury, 2011-09-15 This unique book presents real world success stories of collaboration between mathematicians and industrial partners showcasing first hand case studies and lessons learned from the experiences technologies and business challenges that led to the successful development of industrial solutions based on

mathematics It shows the crucial contribution of mathematics to innovation and to the industrial creation of value and the key position of mathematics in the handling of complex systems amplifying innovation Each story describes the challenge that led to the industrial cooperation how the challenge was approached and how the solutions were achieved and implemented When brought together they illustrate the versatile European landscape of projects in almost all areas of applied mathematics and across all business sectors This book of success stories has its origin in the Forward Look about Mathematics and Industry that was funded by the European Science Foundation ESF and coordinated by the Applied Mathematics Committee of the European Mathematical Society EMS In each of these success stories researchers students entrepreneurs policy makers and business leaders in a range of disciplines will find valuable material and important lessons that can be applied in their own fields

Handbook of Research on Applied Optimization Methodologies in Manufacturing Systems Faruk Yılmaz, Ömer, Tüfekçi, Süleyman, 2017-11-30 Today's manufacturing systems are undergoing significant changes in the aspects of planning production execution and delivery It is imperative to stay up to date on the latest trends in optimization to efficiently create products for the market The Handbook of Research on Applied Optimization Methodologies in Manufacturing Systems is a pivotal reference source including the latest scholarly research on heuristic models for solving manufacturing and supply chain related problems Featuring exhaustive coverage on a broad range of topics such as assembly ratio car sequencing and color constraints this publication is ideally designed for practitioners seeking new comprehensive models for problem solving in manufacturing and supply chain management

A Top-Down, Constraint-Driven Design Methodology for Analog Integrated Circuits Henry Chang, 1997 Analog circuit design is often the bottleneck when designing mixed analog digital systems A Top Down Constraint Driven Design Methodology for Analog Integrated Circuits presents a new methodology based on a top down constraint driven design paradigm that provides a solution to this problem This methodology has two principal advantages 1 it provides a high probability for the first silicon which meets all specifications and 2 it shortens the design cycle A Top Down Constraint Driven Design Methodology for Analog Integrated Circuits is part of an ongoing research effort at the University of California at Berkeley in the Electrical Engineering and Computer Sciences Department Many faculty and students past and present are working on this design methodology and its supporting tools The principal goals are 1 developing the design methodology 2 developing and applying new tools and 3 proving the methodology by undertaking industrial strength design examples The work presented here is neither a beginning nor an end in the development of a complete top down constraint driven design methodology but rather a step in its development This work is divided into three parts Chapter 2 presents the design methodology along with foundation material Chapters 3-8 describe supporting concepts for the methodology from behavioral simulation and modeling to circuit module generators Finally Chapters 9-11 illustrate the methodology in detail by presenting the entire design cycle through three large scale examples These include the design of a current source D/A converter a Sigma Delta A/D converter

and a video driver system Chapter 12 presents conclusions and current research topics A Top Down Constraint Driven Design Methodology for Analog Integrated Circuits will be of interest to analog and mixed signal designers as well as CAD tool developers

Solutions Manual for An Introduction to Digital and Analog Integrated Circuits and

Applications Sanjit K. Mitra, Sanjit Kumar Mitra, 1981 *Electricity and Electronics, Problems and Exercises Manual*

NEWMAN, 1994-12-07 **Parallel Methods for VLSI Layout Design** Si. Pi Ravikumār, 1996 **EDA for IC**

Implementation, Circuit Design, and Process Technology Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03

Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set **Analysis and Design of**

Analog Integrated Circuits Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2024-01-31 ANALYSIS AND

DESIGN OF ANALOG INTEGRATED CIRCUITS Authoritative and comprehensive textbook on the fundamentals of analog integrated circuits with learning aids included throughout Written in an accessible style to ensure complex content can be appreciated by both students and professionals this Sixth Edition of Analysis and Design of Analog Integrated Circuits is a highly comprehensive textbook on analog design offering in depth coverage of the fundamentals of circuits in a single volume To aid in reader comprehension and retention supplementary material includes end of chapter problems plus a Solution Manual for instructors In addition to the well established concepts this Sixth Edition introduces a new super source follower circuit and its large signal behavior frequency response stability and noise properties New material also introduces replica biasing describes and analyzes two op amps with replica biasing and provides coverage of weighted zero value time constants as a method to estimate the location of dominant zeros pole zero doublets including their effect on settling time and three examples of circuits that create doublets the effect of feedback on pole zero doublets and MOS transistor noise performance including a thorough treatment on thermally induced gate noise Providing complete coverage of the subject Analysis and Design of Analog Integrated Circuits serves as a valuable reference for readers from many different types of backgrounds including senior undergraduates and first year graduate students in electrical and computer engineering along with analog integrated circuit designers **Principles of Electronic Circuits** Stanley G. Burns, Paul R. Bond, 1997

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