

III Semester B.E. (E&E) Examination, Dec. 2018/Jan. 2019
(2K11 Scheme)

EE 301 : ANALOG ELECTRONIC CIRCUITS

Time : 3 Hours

Max. Marks : 100

Instruction : Answer any five full questions, by choosing atleast two from each Part.

PART – A

1. a) Classify the diode clipping circuits with neat circuits and relevant waveforms. 8
- b) For the circuit shown below figure 1. b) sketch the transfer characteristic and o/p waveform (Assume ideal diodes). 6

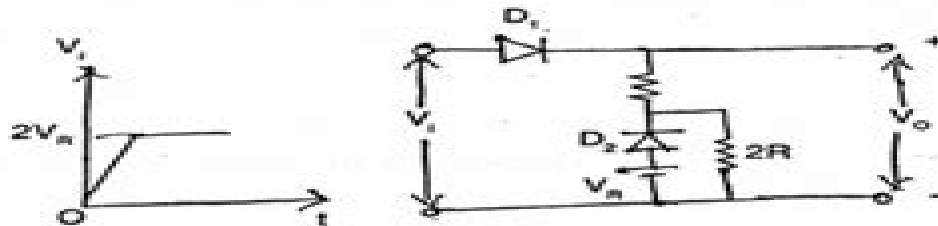


Fig. 1(b)

- c) Define clamping circuit and also explain the working of positive peak clamper. 6
2. a) Define Q-point. Discuss the issues related to stability of operating point of BJT amplifiers. 6
- b) Compare and contrast stability factor of fixed-bias and self-biased BJT circuits. 6
- c) Design a voltage divider bias circuit using a silicon transistor with $V_{CC} = 18V$, $I_C = 2.3 \text{ mA}$, $V_{CE} = 8.2 \text{ V}$, $R_C = 3.3 \text{ K}$, $\beta = 100$ and $S(I_{\infty}) \leq 5$. 8

Analog Electronic Circuit 3rd Sem Ec

Terry C. Jones



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Table of Contents Analog Electronic Circuit 3rd Sem Ec

1. Understanding the eBook Analog Electronic Circuit 3rd Sem Ec
 - The Rise of Digital Reading Analog Electronic Circuit 3rd Sem Ec
 - Advantages of eBooks Over Traditional Books
2. Identifying Analog Electronic Circuit 3rd Sem Ec
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Analog Electronic Circuit 3rd Sem Ec
 - User-Friendly Interface
4. Exploring eBook Recommendations from Analog Electronic Circuit 3rd Sem Ec
 - Personalized Recommendations
 - Analog Electronic Circuit 3rd Sem Ec User Reviews and Ratings
 - Analog Electronic Circuit 3rd Sem Ec and Bestseller Lists

-
5. Accessing Analog Electronic Circuit 3rd Sem Ec Free and Paid eBooks
 - Analog Electronic Circuit 3rd Sem Ec Public Domain eBooks
 - Analog Electronic Circuit 3rd Sem Ec eBook Subscription Services
 - Analog Electronic Circuit 3rd Sem Ec Budget-Friendly Options
 6. Navigating Analog Electronic Circuit 3rd Sem Ec eBook Formats
 - ePub, PDF, MOBI, and More
 - Analog Electronic Circuit 3rd Sem Ec Compatibility with Devices
 - Analog Electronic Circuit 3rd Sem Ec Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Analog Electronic Circuit 3rd Sem Ec
 - Highlighting and Note-Taking Analog Electronic Circuit 3rd Sem Ec
 - Interactive Elements Analog Electronic Circuit 3rd Sem Ec
 8. Staying Engaged with Analog Electronic Circuit 3rd Sem Ec
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Analog Electronic Circuit 3rd Sem Ec
 9. Balancing eBooks and Physical Books Analog Electronic Circuit 3rd Sem Ec
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Analog Electronic Circuit 3rd Sem Ec
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Analog Electronic Circuit 3rd Sem Ec
 - Setting Reading Goals Analog Electronic Circuit 3rd Sem Ec
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Analog Electronic Circuit 3rd Sem Ec
 - Fact-Checking eBook Content of Analog Electronic Circuit 3rd Sem Ec
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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