

EXAMINATION NO.:____ THE MALAWI NATIONAL EXAMINATIONS BOARD

2024 MALAWI SCHOOL CERTIFICATE OF EDUCATION EXAMINATION

MATHEMATICS

Subject Number: M131/II

Friday, 12 July

Time Allowed: 2 h 30 min 8:00 - 10:30 am

PAPER II

(100 marks)

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- This paper contains 19 printed pages. Please check.
- Answer all the six questions in Section A and any four questions from Section B.
- The maximum number of marks for each answer is indicated against each question.
- 4. Scientific calculators may be used.
- The blank answer sheet at the end of the question paper can be used if required.
- 6. All working must be clearly shown.
- Write your Examination Number at the top of each page of your question paper in the spaces provided.
- In the table provided on this page, tick against the question number you have answered.
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numbers a sum s and a carry cout truth table describes the functionality of full adder sum s output is high when odd number of inputs are high cout is high when two or more inputs are high

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let's define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design

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