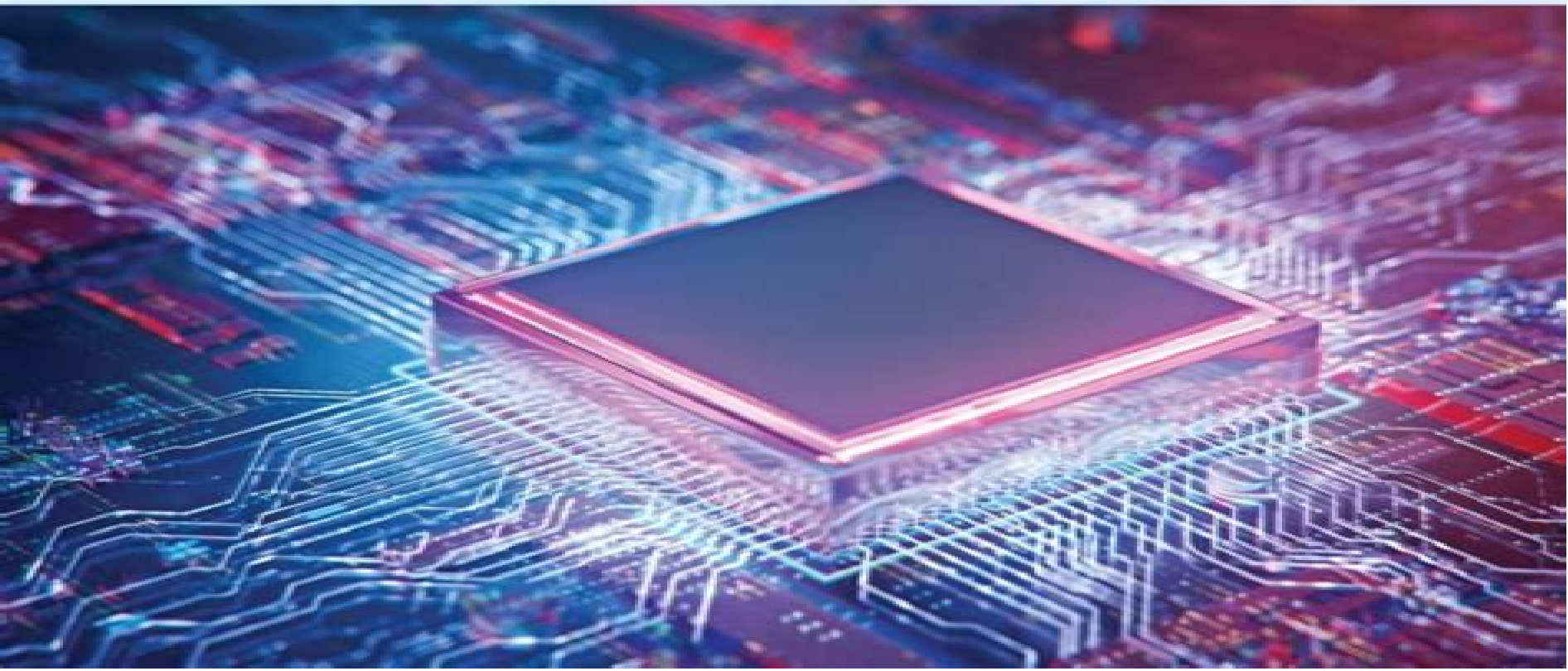


ALGORITHMS FOR VLSI DESIGN AUTOMATION

Johnnie McKenzie



Algorithms For Vlsi Design Automation

John Patrick Lillis



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Algorithms for VLSI Physical Design Automation Naveed A. Sherwani, 2012-12-06 Algorithms for VLSI Physical Design Automation Second Edition is a core reference text for graduate students and CAD professionals Based on the very successful First Edition it provides a comprehensive treatment of the principles and algorithms of VLSI physical design presenting the concepts and algorithms in an intuitive manner Each chapter contains 3 4 algorithms that are discussed in detail Additional algorithms are presented in a somewhat shorter format References to advanced algorithms are presented at the end of each chapter Algorithms for VLSI Physical Design Automation covers all aspects of physical design In 1992 when the First Edition was published the largest available microprocessor had one million transistors and was fabricated using three metal layers Now we process with six metal layers fabricating 15 million transistors on a chip Designs are moving to the 500 700 MHz frequency goal These stunning developments have significantly altered the VLSI field over the cell routing and early floorplanning have come to occupy a central place in the physical design flow This Second Edition introduces a realistic picture to the reader exposing the concerns facing the VLSI industry while maintaining the theoretical flavor of the First Edition New material has been added to all chapters new sections have been added to most chapters and a few chapters have been completely rewritten The textual material is supplemented and clarified by many helpful figures Audience An invaluable reference for professionals in layout design automation and physical design *Algorithms for VLSI Design Automation* Sabih H. Gerez, 1999-01-05 Modern microprocessors such as Intel's Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering **Algorithms Vlsi Design Automation** Gerez, 2006-06 Market_Desc Electrical Engineering Students taking courses on VLSI systems CAD tools for VLSI Design Automation at Final Year or Graduate Level Computer Science courses on the same topics at a similar level Practicing Engineers wishing to learn the state of the art in VLSI Design Automation Designers of CAD tools for chip design in software houses or large electronics companies Special Features Probably the first book on Design Automation for VLSI Systems which covers all stages of design from layout synthesis through logic synthesis to high level synthesis Clear precise presentation of examples well illustrated with over 200 figures Focus on algorithms for VLSI design tools means it will appeal to some Computer Science as well as Electrical Engineering departments About The Book Enrollments in VLSI design automation courses are not large but it's a very popular elective especially for those seeking a career in the microelectronics industry Already the reviewers seem very enthusiastic about the coverage of the book being a better match for their courses than available competitors because it

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