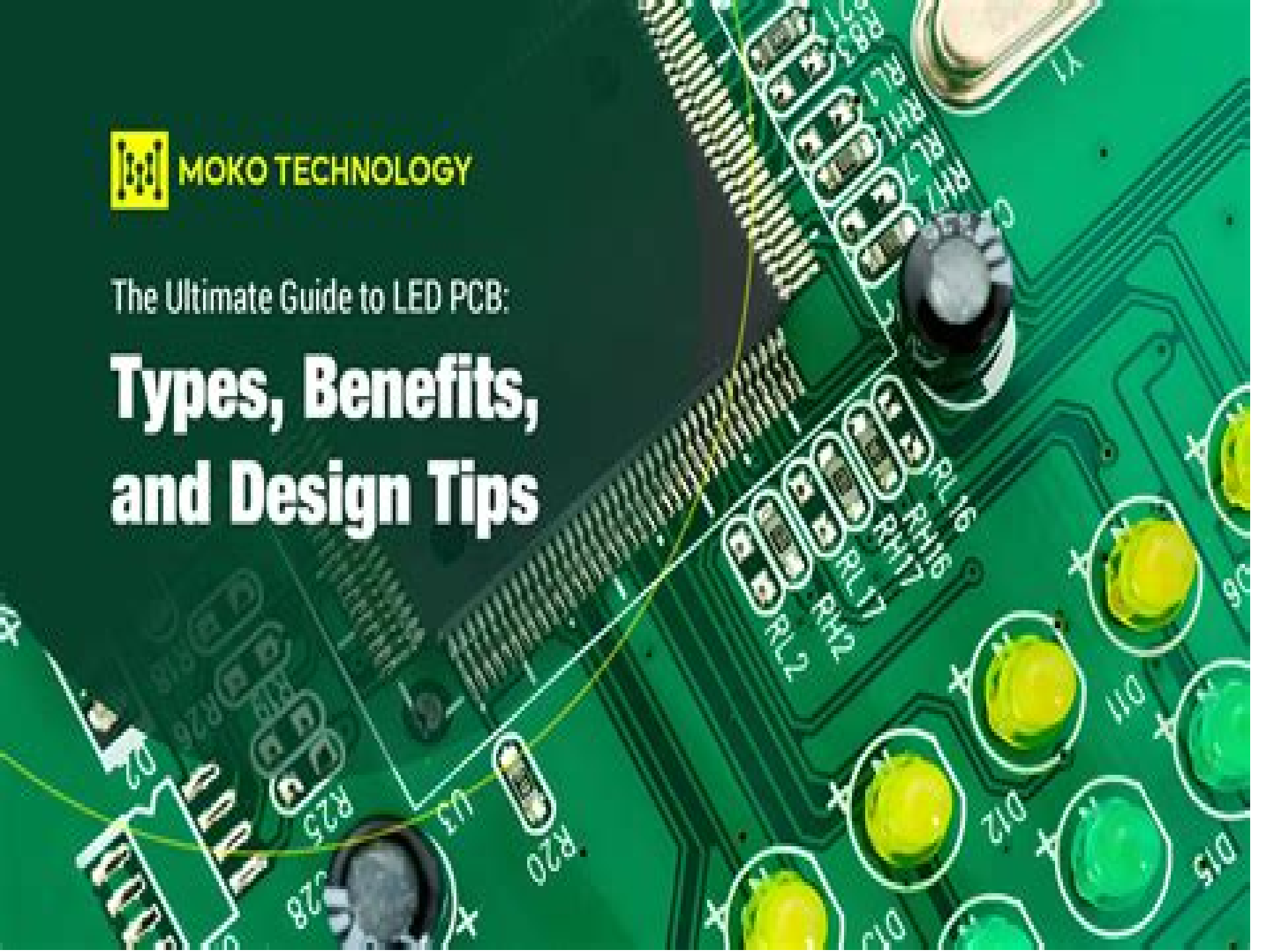




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Complete PCB Design Using OrCad Capture and Layout Kraig Mitzner, 2011-04-01 Complete PCB Design Using OrCad Capture and Layout provides instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards. The book is written for both students and practicing engineers who need a quick tutorial on how to use the software and who need in-depth knowledge of the capabilities and limitations of the software package. There are two goals the book aims to reach. The primary goal is to show the reader how to design a PCB using OrCAD Capture and OrCAD Layout. Capture is used to build the schematic diagram of the circuit and Layout is used to design the circuit board so that it can be manufactured. The secondary goal is to show the reader how to add PSpice simulation capabilities to the design and how to develop custom schematic parts footprints and PSpice models. Often times separate designs are produced for documentation simulation and board fabrication. This book shows how to perform all three functions from the same schematic design. This approach saves time and money and ensures continuity between the design and the manufactured product. Information is presented in the exact order a circuit and PCB are designed. Straightforward realistic examples present the how and why the designs work providing a comprehensive toolset for understanding the OrCAD software. Introduction to the IPC JEDEC and IEEE standards relating to PCB design. Full color interior and extensive illustrations allow readers to learn features of the product in the most realistic manner possible.

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designing a PCB first for connectivity and secondly introduces the four most important principles to reduce noise A seven step process is presented developing a plan of record creating a Bill of Materials completing the schematic completing the layout completing the assembly conducting bring up and troubleshooting and documenting the project Each step is developed in detail In particular the emphasis in this book is on risk management what can be done at each step of the process to reduce the risk of a hard error which requires a complete re spin or a soft error which requires some sort of on the fly repair After connectivity is designed it s important to develop good habits to minimize the potential noise from ground bounce power rail stitching noise stack up design and reducing switching noise in signal paths These techniques apply to all designs from 2 layer to 8 layer and more for bandwidths below 200 MHz The best practices for manual lead free soldering are presented so that everyone can become a soldering expert The best measurement practices using common lab instruments such as the DMM the constant current constant voltage power supply and oscilloscopes are presented so that common artifacts are minimized Features in the design that help you find design or assembly errors quickly and the troubleshooting techniques to find and fix problems are introduced Applying the habits presented in this book will help every engineer design their next circuit board faster with less chance of an unexpected problem with the lowest noise This textbook will also have embedded videos to visually demonstrate many of the hands on processes introduced in this book

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