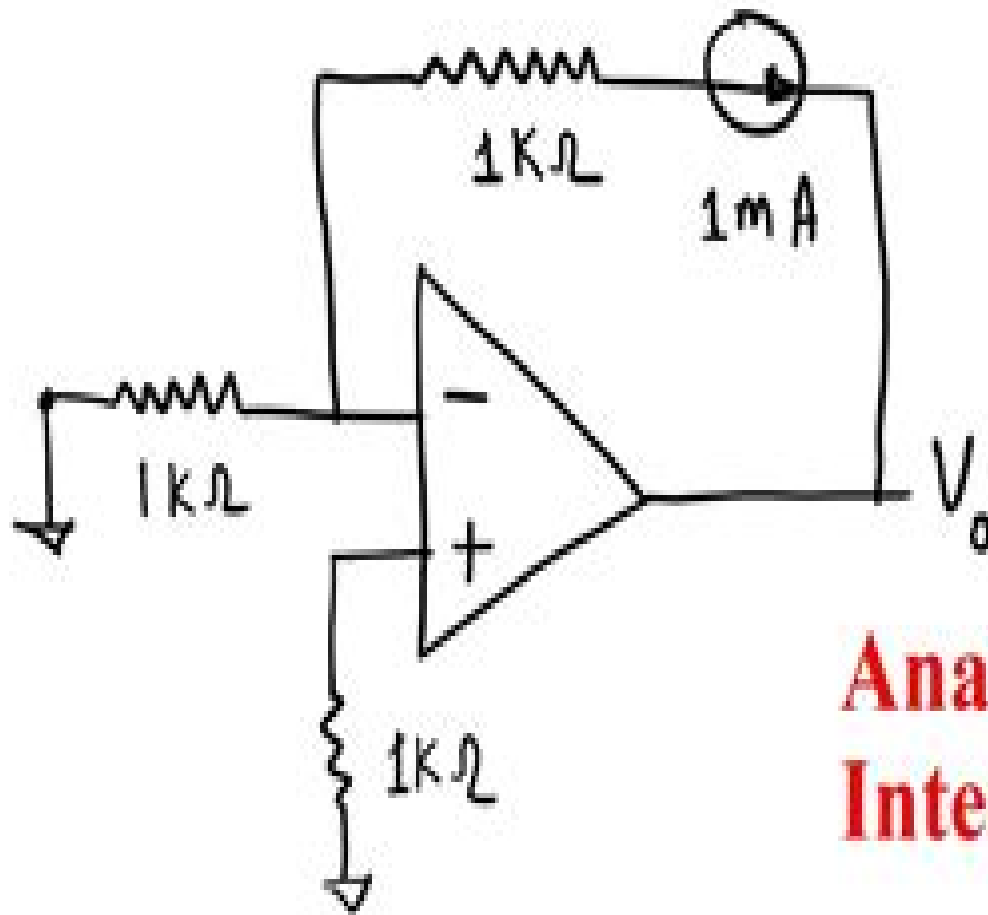


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A D D A converters Symmetry principles and associated circuit constraints structures and methods are adopted to mitigate VLSI PVT and other variations for better circuit performance functionality and design productivity across multiple VLSI process nodes

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discussed along with the available tools for FPGA prototyping as well as ASIC design Each unit contains technical questions with solutions at the end Technical topics discussed in the book include Static Timing Analysis CMOS Layout and Design rules Physical Design Automation Testing of VLSI Circuits Software tools for Frontend and Backend design

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