



Caterpillar **Service Manual** **3406 Diesel** **Engine** **S/n 70V1 & Up**



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Service Manual

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Didier Musso



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web frame size 36 36 36mm 03 38 38mm 04 40 40mm 05 52 52mm 06 60 60mm 08 80 80mm 09 92 92mm 12 120 120mm 13 127 127mm 14 140 140mm 15 150 150mm 17 ϕ 172mm 47 ϕ 172mm 147mm sidecut 57 ϕ 172mm 150mm sidecut 20 ϕ 200mm voltage 05 5v 12 12v 24 24v 48 48v etc speed code

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web sep 9 2018 issue 2 many time we see the performance of the fin fan cooler is not as ok and unable to maintain the proper cooling it is because of the many reason e g fouling of the tubes inside of the

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web life of the cooler this is the preferred tube for operating temperatures up to 600 f the embedded fin also affords a continued predictable heat transfer and should be used for all coolers operating above 600 f and below 750 f the wrap on footed fin tube can be used below 250 f however the bond between the fin

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web introduction fin fan cooler is a kind of heat exchanger that uses the air for cooling medium it is required a wide surface area for heat transfer it is applied to finned tube for increasing the heat transfer surface and compact design of construction area and the process fluid can be cooled by the natural air it is not required a coolant

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web our large fan field erected units are available in bay sizes up to 70 feet by 54 feet and fan diameters up to 36 feet our modular design and construction expertise allows for fast and dependable field erection benefits of the large fan design include air cooled heat exchanger air heat exchanger heat exchanger

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web the air cooler bank essentially consists of finned tube bundles fan rings axial fans with suitable drive units and a supporting structure it can be equipped individually with maintenance platforms louvers and other equipment as requested by the customer

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web the i 2c module contains an independent i 2c master logic and a i 2c slave logic which generates interrupts based on their events in the multi master systems the user software is simply partitioned into the master controller and the slave

controller when the i2c master logic is active the slave logic also remains active detecting the
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web i2c master h this header contains i/o definitions selection and timing clock speed settings used for the master i2c interface this file also includes prototypes of master i2c functions i2c master c this source file contains master i2c function implementations file

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web i2c module modes and features the i2c module provides the following operational modes and features master mode slave mode with byte nacking multi master mode dedicated receive and transmit buffers up to four dedicated slave address registers 1

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web the i2c master is byte oriented and interrupt based the number of interrupts generated is kept at a minimum by automatic handling of most incidents the software driver complexity and code size are reduced by auto triggering of operations and a special smart mode which can be enabled by the smart mode enable bit in the control b register

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web this example shows how the microcontroller configured in i2c host mode writes to and reads data from an MCP23008 8 bit i2c i/o expander client device addressed in 7 bit mode using interrupts

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web control of the i2c bus may be taken when the p bit is set or the bus is idle in firmware controlled master mode user code conducts all i2c bus operations based on start and stop bit condition detection start and stop condition detection is

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web lbidirectional i2c stands for inter integrated circuit communications i2c is implemented in the PICmicro by a hardware module called the master synchronous serial port known as the mssp module

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