

EZ-KIT Lite for Analog Devices ADSP-BF533 Blackfin Processor

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Key Features

- ADSP-BF533 Blackfin Processor
- 64 MB (32M × 16-bit) SDRAM
- 2 MB (512k × 16-bit × 2) flash memory
- AD1836 96 kHz audio codec with 4 input and 6 output RCA jacks
- ADV7183 video decoder with 3 input RCA jacks
- ADV7171 video encoder with 3 output RCA jacks
- ADM3202 RS-232 line driver/receiver
- DB9 male connector
- USB-based debugger interface
- JTAG ICE 14-pin header
- SPI connector
- SPORT1 connector
- Background telemetry channel
- Evaluation suite of VisualDSP++
- 10 LEDs: 1 power, 1 board reset, 1 USB reset, 1 USB monitor, 6 general purpose
- 5 push-buttons with debounce logic: 1 reset, 4 programmable flag
- Three 90-pin connectors providing PPI, SPI, EBIU Timers0-2, UART, programmable flags, SPORT0, and SPORT1 expansion interfaces for analyzing and interfacing
- CE certified

System Requirements

- Pentium® 500 MHz or higher
- Minimum of 256 MB of RAM
- Windows® 2000 or Windows XP
- One available USB connector
- 750 MB disk space



Overview

The ADSP-BF533 EZ-KIT Lite® provides developers with a cost-effective method for initial evaluation of the ADSP-BF533 Blackfin® Processor for a wide range of applications, including audio and video processing. The EZ-KIT Lite includes an ADSP-BF533 desktop evaluation board and fundamental debugging software to facilitate architecture evaluations via a USB-based PC-hosted tool set. Real-time debugging is made possible via the background telemetry channel (BTC) feature. Through BTC, data can be streamed both to and from the processor over the JTAG connection between host and embedded processor without the overhead involved with halting the target application, getting the desired information, and then restarting the target application. With this EZ-KIT Lite, users can learn more about ADI's ADSP-BF533 hardware and software development and prototype applications.

The ADSP-BF533 EZ-KIT Lite provides an evaluation suite of the VisualDSP++® integrated development and debugging environment with C/C++ compiler, advanced plotting tools, statistical profiling, and the VisualDSP++ kernel (VDK). Other features of VisualDSP++ include: assembler, linker, libraries, and splitter. VisualDSP++ offers programmers a powerful programming tool with flexibility that significantly reduces the time to market. The VisualDSP++ software included with the EZ-KIT Lite is limited in program memory size for use solely with the EZ-KIT Lite product.

Analog Devices (ADI) has EZ-Extender® products (sold separately) that plug into the expansion interface of the ADSP-BF533 EZ-KIT Lite for additional functionality. The Blackfin EZ-Extender daughter board allows developers to connect to a number of Analog Devices high speed converter (HSC) evaluation boards, the OV6630 OmniVision camera evaluation board, and an external LCD display. The Blackfin USB-LAN EZ-Extender daughter board contains a USB 2.0 interface and 10/100 Ethernet MAC.

Adsp Bf533 Ez Kit Lite Evaluation System Manual

Woon-Seng Gan, Sen M. Kuo



Adsp Bf533 Ez Kit Lite Evaluation System Manual:

Embedded Signal Processing with the Micro Signal Architecture Woon-Seng Gan, Sen M. Kuo, 2007-02-26 This is a real time digital signal processing textbook using the latest embedded Blackfin processor Analog Devices Inc ADI 20% of the text is dedicated to general real time signal processing principles The remaining text provides an overview of the Blackfin processor its programming applications and hands on exercises for users With all the practical examples given to expedite the learning development of Blackfin processors the textbook doubles as a ready to use user s guide The book is based on a step by step approach in which readers are first introduced to the DSP systems and concepts Although basic DSP concepts are introduced to allow easy referencing readers are recommended to complete a basic course on Signals and Systems before attempting to use this book This is also the first textbook that illustrates graphical programming for embedded processor using the latest LabVIEW Embedded Module for the ADI Blackfin Processors A solutions manual is available for adopters of the book from the Wiley editorial department

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