



Figure P3.10

Since gate voltage is high, the midpoint V_x is expected to be low. Therefore, the load is in saturation and the driver is in linear region. From KCL

$$I_D = I_{D,driver} = I_{D,load}$$

$$\frac{1}{2} k' \frac{W}{L} (1 V - V_x)^2 = \frac{1}{2} k' \frac{W'}{L'} (2.1 V - V_x)^2$$

Using the following two equations to iterate find the solution.

$$1 V - V_{T,L} - (V')^2 = 1.04 V_x - V_x^2$$

$$V_{T,L}(V_x) = 0.48 - 0.52 \sqrt{1.01 V_x} \quad \sqrt{1.01}$$

The intermediate values are listed in the table:

$V_{T,L}(V_x)$	V_x
0.480	0.1523
0.518	0.1337
0.513	0.1359
0.514	0.1357
0.514	0.1357

$$I_D = \frac{1}{2} k' \frac{W}{L} (1.04 V_x - V_x^2) = 0.516810 (1.04 \cdot 0.1357 - 0.1357^2) = 103.1 [\text{A}]$$

3.11 The following parameters are given for an nMOS process:

$t_{ox} = 16 \text{ \AA}$

substrate doping $N_a = 4 \cdot 10^{15} \text{ cm}^{-3}$

polysilicon gate doping $N_p = 2 \cdot 10^{19} \text{ cm}^{-3}$

oxide-interface fixed-charge density $N_{ox} = 2 \cdot 10^{11} \text{ cm}^{-2}$

(a) Calculate V_T for an unimplanted transistor.

(b) What type and what concentration of impurities must be implanted to achieve $V_{T_p} = +0.6 \text{ V}$ and $V_{T_n} = -0.6 \text{ V}$?

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Sebastian Brünink



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1. Understanding the eBook Analysis And Design Of Digital Integrated Circuits Solution Manual
 - The Rise of Digital Reading Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Analysis And Design Of Digital Integrated Circuits Solution Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Personalized Recommendations
 - Analysis And Design Of Digital Integrated Circuits Solution Manual User Reviews and Ratings
 - Analysis And Design Of Digital Integrated Circuits Solution Manual and Bestseller Lists
5. Accessing Analysis And Design Of Digital Integrated Circuits Solution Manual Free and Paid eBooks
 - Analysis And Design Of Digital Integrated Circuits Solution Manual Public Domain eBooks
 - Analysis And Design Of Digital Integrated Circuits Solution Manual eBook Subscription Services
 - Analysis And Design Of Digital Integrated Circuits Solution Manual Budget-Friendly Options
6. Navigating Analysis And Design Of Digital Integrated Circuits Solution Manual eBook Formats

- ePub, PDF, MOBI, and More
- Analysis And Design Of Digital Integrated Circuits Solution Manual Compatibility with Devices
- Analysis And Design Of Digital Integrated Circuits Solution Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Highlighting and Note-Taking Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Interactive Elements Analysis And Design Of Digital Integrated Circuits Solution Manual
- 8. Staying Engaged with Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Analysis And Design Of Digital Integrated Circuits Solution Manual
- 9. Balancing eBooks and Physical Books Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Analysis And Design Of Digital Integrated Circuits Solution Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Setting Reading Goals Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Fact-Checking eBook Content of Analysis And Design Of Digital Integrated Circuits Solution Manual
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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