3. Balanced Modulator

Aim:

- To construct and properly adjust a balanced modulator and study its operation.
- To observe that the output is a double sideband with a suppressed carrier signal.
- To adjust it for optimum carrier suppression.
- To verify that the output audio level directly affects the double side-band output amplitude.
- 5. To observe that the output is maximum with zero audio input.
- To measure carrier only output and the peak side- band output and to calculate the carrier suppression.

Equipment Required:

Analog communication kit, CRO connecting wires.

Component Required:

- 1. IC'S:1496- (1no)
- Resistors:100Ω-(1no),820Ω-(1no),1kΩ-(1no)1.2kΩ-(1no),2.7kΩ-(1no),10kΩ-(3nos),47kΩ-(1nos),50kΩ-POT
- Capacitors: 1μF-(1no),22μF-(1no),100μF-(2nos)

Theory:

One circuit that lends itself extremely well to balanced modulator application is the differential amplifier. A simplified diagram of a differential amplifier is shown in figure 1.



FIG 1: A Differential amplifier used as a balanced modulator

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- 1. Understanding the eBook Analog Communication Lab Syllabus With Manual
 - The Rise of Digital Reading Analog Communication Lab Syllabus With Manual
 - Advantages of eBooks Over Traditional Books
- 2. Identifying Analog Communication Lab Syllabus With Manual
 - Exploring Different Genres
 - $\circ\,$ Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Analog Communication Lab Syllabus With Manual
 - User-Friendly Interface
- 4. Exploring eBook Recommendations from Analog Communication Lab Syllabus With Manual

- Personalized Recommendations
- Analog Communication Lab Syllabus With Manual User Reviews and Ratings
- Analog Communication Lab Syllabus With Manual and Bestseller Lists
- 5. Accessing Analog Communication Lab Syllabus With Manual Free and Paid eBooks
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 - Analog Communication Lab Syllabus With Manual eBook Subscription Services
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 - Analog Communication Lab Syllabus With Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Analog Communication Lab Syllabus With Manual
 - Highlighting and Note-Taking Analog Communication Lab Syllabus With Manual
 - o Interactive Elements Analog Communication Lab Syllabus With Manual
- 8. Staying Engaged with Analog Communication Lab Syllabus With Manual
 - o Joining Online Reading Communities
 - $\circ \ \ Participating \ in \ Virtual \ Book \ Clubs$
 - Following Authors and Publishers Analog Communication Lab Syllabus With Manual
- 9. Balancing eBooks and Physical Books Analog Communication Lab Syllabus With Manual
 - Benefits of a Digital Library
 - o Creating a Diverse Reading Collection Analog Communication Lab Syllabus With Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Analog Communication Lab Syllabus With Manual
 - Setting Reading Goals Analog Communication Lab Syllabus With Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Analog Communication Lab Syllabus With Manual

- Fact-Checking eBook Content of Analog Communication Lab Syllabus With Manual
- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
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web the risk maturity model rmm outlines key indicators and activities that comprise a sustainable repeatable and mature enterprise risk management erm program taking the risk maturity self assessment organizations benchmark how in line their current risk management practices are with the rmm indicators once completed each organization