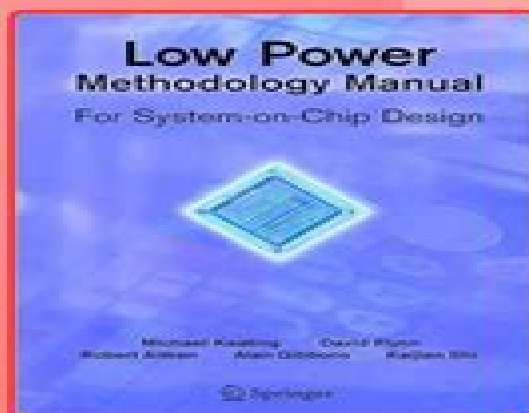


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An ASIC Low Power Primer Rakesh Chadha, J. Bhasker, 2012-12-05 This book provides an invaluable primer on the techniques utilized in the design of low power digital semiconductor devices Readers will benefit from the hands on approach which starts from the ground up explaining with basic examples what power is how it is measured and how it impacts on the design process of application specific integrated circuits ASICs The authors use both the Unified Power Format UPF and Common Power Format CPF to describe in detail the power intent for an ASIC and then guide readers through a variety of architectural and implementation techniques that will help meet the power intent From analyzing system power consumption to techniques that can be employed in a low power design to a detailed description of two alternate standards for capturing the power directives at various phases of the design this book is filled with information that will give ASIC designers a competitive edge in low power design

Design and Modeling of Low Power VLSI Systems Sharma, Manoj, Gautam, Ruchi, Khan, Mohammad Ayoub, 2016-06-06 Very Large Scale Integration VLSI Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip Emerging research in this area has the potential to uncover further applications for VLSI technologies in addition to system advancements Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization Through a research based discussion of the technicalities involved in the VLSI hardware development process cycle this book is a useful resource for researchers engineers and graduate level students in computer science and engineering

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Rabaey,2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer The book lays the foundation with background chapters entitled Advanced MOS Transistors and Their Models and Power Basics These chapters are followed by chapters on the design process including optimization architecture and algorithm level memory run time standby logic and standby memory Chapters on special topics are also included power management and modal design ultra low power and low power design methodology and flows The book concludes with a chapter on case studies as well as a chapter on Projection into the Future These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide All chapters have been drawn up specifically for self study They aim however at different levels of understanding All the chapters start with elementary material but most also contain advanced material

Low-Power VLSI Circuits and Systems Ajit Pal,2014-11-17 The book provides a comprehensive coverage of different aspects of low power circuit synthesis at various levels of design hierarchy starting from the layout level to the system level For a seamless understanding of the subject basics of MOS circuits has been introduced at transistor gate and circuit level followed by various low power design methodologies such as supply voltage scaling switched capacitance minimization techniques and leakage power minimization approaches The content of this book will prove useful to students researchers as well as practicing engineers

Power-Aware Testing and Test Strategies for Low Power Devices Patrick Girard,Nicola Nicolici,Xiaoqing Wen,2010-03-11 Managing the power consumption of circuits and systems is now considered one of the most important challenges for the semiconductor industry Elaborate power management strategies such as dynamic voltage scaling clock gating or power gating techniques are used today to control the power dissipation during functional operation The usage of these strategies has various implications on manufacturing test and power aware test is therefore increasingly becoming a major consideration during design for test and test preparation for low power devices This book explores existing solutions for power aware test and design for test of conventional circuits and systems and surveys test strategies and EDA solutions for testing low power devices

Introduction to Low-Power Design in VLSIs Patrick Lee,2011-12-12 This book discusses one increasingly important issue in the VLSI design low power It covers the following topics a basic concepts of low power design b low power design methods and applications in industry chips and c commercial CAD tools on low power design This book discusses the concepts a set of known methods industry cases and CAD tools on the low power design It is organized in four chapters and a glossary is provided at the end of the book

Ultra-Low Power Integrated Circuit Design Nianxiong Nick Tan,Dongmei Li,Zhihua Wang,2013-10-23 This book describes the design of CMOS circuits for ultra low power consumption including analog radio frequency RF and digital signal processing circuits DSP The book addresses issues from circuit and system design to production design and applies the ultra low power circuits described to systems for digital hearing aids and capsule endoscope devices Provides a valuable introduction to ultra low power circuit design aimed at practicing design engineers Describes all key building blocks of ultra low power circuits from a

systems perspective Applies circuits and systems described to real product examples such as hearing aids and capsule endoscopes

Architecture of Computing Systems - ARCS 2010 Christian Müller-Schloer, Wolfgang Karl, Sami Yehia, 2010-04-07 Annotation This book constitutes the refereed proceedings of the 23rd International Conference on Architecture of Computing Systems ARCS 2010 held in Hannover Germany in February 2010 The 20 revised full papers presented together with 1 keynote lecture were carefully reviewed and selected from 55 submissions This year's special focus is set on heterogeneous systems The papers are organized in topical sections on processor design embedded systems organic computing and self organization processor design and transactional memory energy management in distributed environments and ad hoc grids performance modeling and benchmarking as well as accelerators and GPUs

Recent Trends in Communication and Intelligent Systems Aditya Kumar Singh Pundir, Neha Yadav, Harish Sharma, Swagatam Das, 2022-05-24 This book presents best selected research papers presented at the Third International Conference on Recent Trends in Communication and Intelligent Systems ICRTCIS 2021 organized by Arya College of Engineering and IT Jaipur on 22-23 October 2021 It discusses the latest technologies in communication and intelligent systems covering various areas of communication engineering such as signal processing VLSI design embedded systems wireless communications and electronics and communications in general Featuring work by leading researchers and technocrats the book serves as a valuable reference resource for young researchers and academics as well as practitioners in industry

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