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Yu Hen Hu



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High Performance Embedded Computing Handbook David R. Martinez, Robert A. Bond, M. Michael Vai, 2018-10-03 Over the past several decades applications permeated by advances in digital signal processing have undergone unprecedented growth in capabilities The editors and authors of High Performance Embedded Computing Handbook A Systems Perspective have been significant contributors to this field and the principles and techniques presented in the handbook are reinforced by examples drawn from their work The chapters cover system components found in today s HPEC systems by addressing design trade offs implementation options and techniques of the trade then solidifying the concepts with specific HPEC system examples This approach provides a more valuable learning tool Because readers learn about these subject areas through factual implementation cases drawn from the contributing authors own experiences Discussions include Key subsystems and components Computational characteristics of high performance embedded algorithms and applications Front end real time processor technologies such as analog to digital conversion application specific integrated circuits field programmable gate arrays and intellectual property based design Programmable HPEC systems technology including interconnection fabrics parallel and distributed processing performance metrics and software architecture and automatic code parallelization and optimization Examples of complex HPEC systems representative of actual prototype developments Application examples including radar communications electro optical and sonar applications The handbook is organized around a canonical framework that helps readers navigate through the chapters and it concludes with a discussion of future trends in HPEC systems The material is covered at a level suitable for practicing engineers and HPEC computational practitioners and is easily adaptable to their own implementation requirements *ICSP2002* Yuan Baozong, 2002

Embedded and Multimedia Computing Technology and Service James J. (Jong Hyuk) Park, Young-Sik Jeong, Sang Oh Park, Hsing-Chung Chen, 2012-08-31 The 7th International Conference on Embedded and Multimedia

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Programmable Digital Signal

Processors Yu Hen Hu, 2001-12-06 Presents the latest developments in the programming and design of programmable digital signal processors PDSPs with very long instruction word VLIW architecture algorithm formulation and implementation and modern applications for multimedia processing communications and industrial control

Computer Engineering and

Technology Weixia Xu, Liquan Xiao, Jinwen Li, Chengyi Zhang, Zhenzhen Zhu, 2015-02-26 This book constitutes the refereed proceedings of the 18th National Conference on Computer Engineering and Technology NCCET 2014 held in Guiyang China during July August 2014 The 18 papers presented were carefully reviewed and selected from 85 submissions They are organized in topical sections on processor architecture computer application and software optimization technology on the horizon *Cutting Edge Robotics*, 2010

Field-Programmable Logic and Applications

Gordon Brebner, 2001-08-15 This book constitutes the refereed proceedings of the 11th International Conference on Field Programmable Logic and Application FPL 2001 held in Belfast Northern Ireland UK in August 2001 The 56 revised full papers and 15 short papers presented were carefully reviewed and selected from a total of 117 submissions The book offers topical sections on architectural framework place and route architecture DSP synthesis encryption runtime reconfiguration graphics and vision networking processor interaction applications methodology loops and systolic image processing faults and arithmetic

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