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	CRAPHICS	NVMe (+PCle* CPP) or to	USABLE POH' LANES TOTAL/POH' S.B SP10	RYZEN PROCESSOR OVERCLOCKING ENABLED	DORS MEMORY OVERCLOCKING ENABLED EUROPS AND DIPCT	USB 20Chps Sur 100	958 1058ps 6P 10	USB SChos 64 TO	MAXIMUM SATA PORTS (OR PCW* 3.0) (M*10)
**X670	1x16 or 2x8 PCIe* 5.0	1x4 POe* 5.0 + 4x PCle* GPP	44/24	Yes	Yes	2	12		8
** X670	1x16 or 2x8 PCIe* 4.0	1x4 PCle* 5.0 + 4x PCle* GPP	44/8	Yes	Yes	2	12		8
***B650	1x16 or 2x8 PCle* 5.0	1x4 PCle* 5.0 + 4x PCle* GPP	36/24	Yes	Yes	1	6		4:
**B650	1x16 or 2x8 PCIe* 4.0	te4 PCIe* 4.0 (PCIe* 5.0 Optional)	36/0	Yes	Yes	1	6		4
***A620	1x16 P(le* 4.0	1x4 PC(e* 4.0	<b>32/</b> 0	No	Yes		2	2	4

# **Amd Processors Guide**

**Lauren Gardner** 

#### **Amd Processors Guide:**

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web die u6 verbindet in nord süd richtung tegel mit mariendorf berührt hierbei den wedding das Östliche zentrum kreuzberg und tempelhof die linie ist 20 kilometer lang und hat 29 bahnhöfe sie war 1923 die erste linie die von der stadt berlin als so genannte großprofillinie gebaut wurde

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web von der seestraße in richtung norden bis kurt schumacher platz 03 05 1956 endet die linie u6 heute in alt tegel 31 05 1958 der südliche abschnitt von tempelhof bis alt mariendorf der heutige entbahnhof wurde am 28 02 1966 dem öffentlichen verkehr freigegeben die u6 ist mit 29 bahnhöfen von denen 3 oberirdisch sind eine

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