

Fundamentals of Logic Design

Dr. Kiran Bailey

Procedural Blocks

always and initial

Most of the programming languages (e.g. C, Basic) are **sequential** in nature, or only one active process at any one time. However, hardware circuitry is **concurrent** in nature. All the circuitries are active in parallel.

Verilog supports parallelism by allowing any number of “**always**” and “**initial**” blocks. Each “**always**” and “**initial**” block run **concurrently**.

“**always**” and “**initial**” blocks are called **procedural blocks**. Assignment inside procedural blocks are called **procedural assignment**.

always and initial (cont)

Features procedural assignment

Answer Guide To Fundamentals Of Logic Design

M Walker



Answer Guide To Fundamentals Of Logic Design:

This book delves into Answer Guide To Fundamentals Of Logic Design. Answer Guide To Fundamentals Of Logic Design is an essential topic that needs to be grasped by everyone, from students and scholars to the general public. The book will furnish comprehensive and in-depth insights into Answer Guide To Fundamentals Of Logic Design, encompassing both the fundamentals and more intricate discussions.

1. The book is structured into several chapters, namely:
 - Chapter 1: Introduction to Answer Guide To Fundamentals Of Logic Design
 - Chapter 2: Essential Elements of Answer Guide To Fundamentals Of Logic Design
 - Chapter 3: Answer Guide To Fundamentals Of Logic Design in Everyday Life
 - Chapter 4: Answer Guide To Fundamentals Of Logic Design in Specific Contexts
 - Chapter 5: Conclusion
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 6. In chapter 5, the author will draw a conclusion about Answer Guide To Fundamentals Of Logic Design. The final chapter will summarize the key points that have been discussed throughout the book.
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Answer Guide To Fundamentals Of Logic Design Introduction

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