

**THE UNIVERSITY OF TEXAS AT AUSTIN
DEPARTMENT OF ELECTRICAL ENGINEERING
EE 360K: DIGITAL SYSTEMS LAB**

Experiment 8.1 – Counter with Register and Register with Counter, with 74181 ALU and 74180 MUX

1. Introduction: This circuit is intended as a classroom exercise in which, beyond logic and computer-aided logic, you will be combining various logic gates beyond merely in the lab exercise. You should already know enough about logic gates to be able to construct a circuit that implements a logic function using only logic gates.

2. Objectives: The purpose of Experiment 8.1 is to design a circuit that implements a counter with register and register with counter. You will be required to design a circuit that implements a counter with register and register with counter. You will be required to design a circuit that implements a counter with register and register with counter.

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- 1. The 74181 ALU, which is a 4-bit ALU (Arithmetic Logic Unit) with 16 inputs and 16 outputs.
- 2. The 74180 MUX, which is a 4-bit MUX (Multiplexer) with 16 inputs and 16 outputs.
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Answers For Digital Fundamentals Experiment

Peng Qi,Zhikui Chen



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