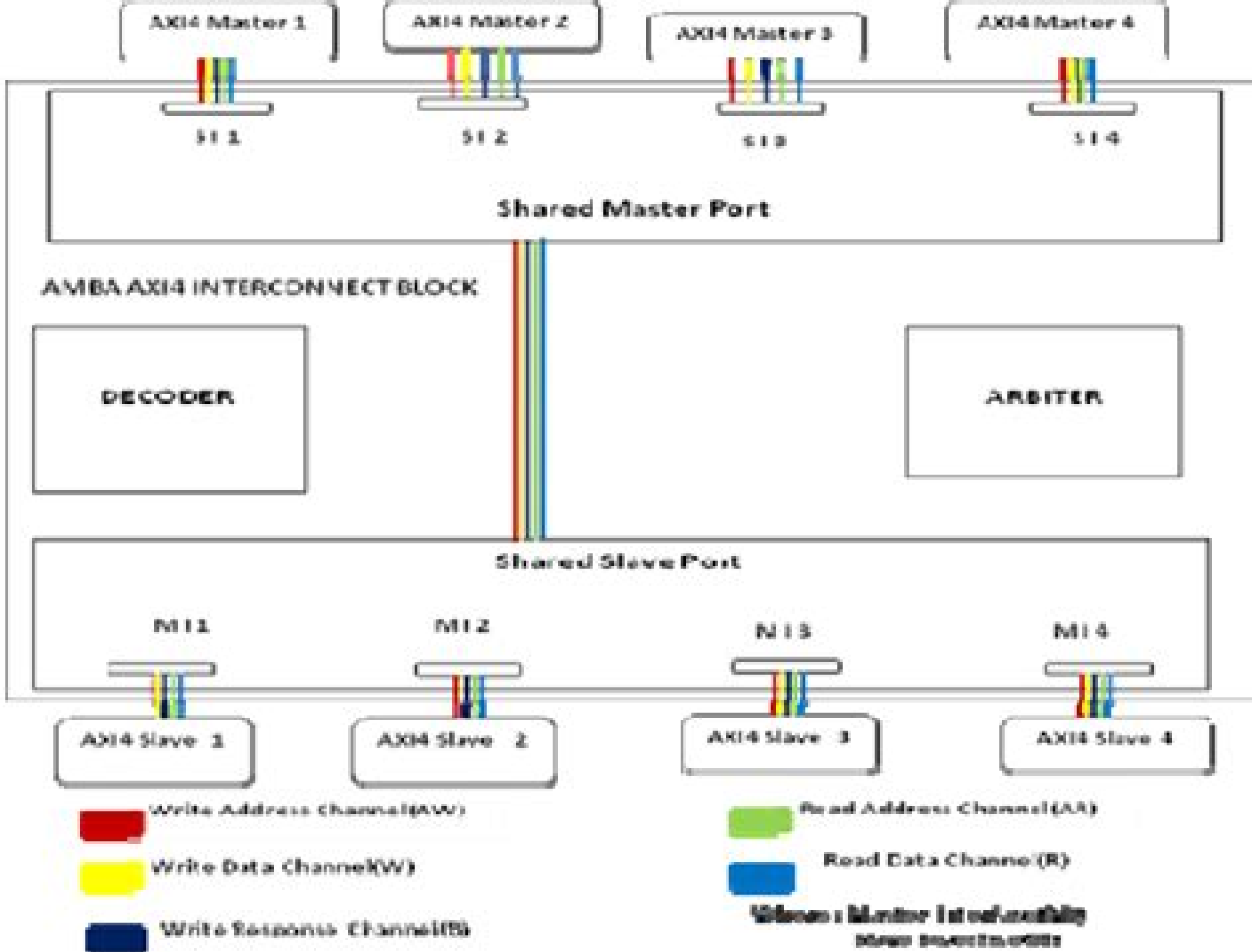




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José L. Ayala

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design industry *Handbook of Computer Architecture* Anupam Chattopadhyay,2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications **Applications in Electronics Pervading Industry,**

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L. Ayala,2018-09-03 A presentation of state of the art approaches from an industrial applications perspective Communication Architectures for Systems on Chip shows professionals researchers and students how to attack the problem of data communication in the manufacture of SoC architectures With its lucid illustration of current trends and research improving the performance quality and reliability of transactions this is an essential reference for anyone dealing with communication mechanisms for embedded systems systems on chip and multiprocessor architectures or trying to overcome existing limitations Exploring architectures currently implemented in manufactured SoCs and those being proposed this book analyzes a wide range of applications including Well established communication buses Less common networks on chip Modern technologies that include the use of carbon nanotubes CNTs Optical links used to speed up data transfer and boost both security and quality of service QoS The book s contributors pay special attention to newer problems including how to protect transactions of critical on chip information personal data security keys etc from an external attack They examine mechanisms revise communication protocols involved and analyze overall impact on system performance **Robotics and Mechatronics** Chin-Hsing Kuo,Pei-Chun Lin,Terence Essomba,Guan-Chen Chen,2019-09-26 This book gathers the latest

advances innovations and applications in the field of robotics and mechatronics as presented by leading international researchers and engineers at the 6th IFToMM International Symposium on Robotics and Mechatronics ISRMM held in Taipei Taiwan on October 28 30 2019 It covers highly diverse topics including mechanism synthesis analysis and design kinematics and dynamics of multibody systems modelling and simulation sensors and actuators novel robotic systems industrial and service related robotics and mechatronics medical robotics and historical developments in robotics and mechatronics The contributions which were selected through a rigorous international peer review process share exciting ideas that spur novel research directions and foster new multidisciplinary collaborations

FPGA to High speed ADC Data streaming Marco Gottardo, 2018 Where does the content of this book apply Firstly in research institutes where it is necessary to acquire data in streaming at high speed and low noise especially in the lower part of the spectrum For example the current machines for the study of nuclear fusion does not produce energy and their output is substantially a large amount of data The accuracy of the data collected and their density within narrow temporal samples can determine the effectiveness of the real time control systems to install in future reactors We set ourselves the objective to design and test a high speed and high density data acquisition system based on the latest generation FPGA technologies in the book is used the latest products released by Xilinx to design a acquire stream system of signals from generic probes specifically magnetic probes The Zynq 7000 family is nowadays state of the art of sistemy SoC that integrating a powerful and extensive FPGA section with an ARM multicore

Image Processing Using FPGAs Donald Bailey, 2019-06-11 This book presents a selection of papers representing current research on using field programmable gate arrays FPGAs for realising image processing algorithms These papers are reprints of papers selected for a Special Issue of the Journal of Imaging on image processing using FPGAs A diverse range of topics is covered including parallel soft processors memory management image filters segmentation clustering image analysis and image compression Applications include traffic sign recognition for autonomous driving cell detection for histopathology and video compression Collectively they represent the current state of the art on image processing using FPGAs

Complex Digital Hardware Design Istvan Nagy, 2024-05-09 This book is about how to design the most complex types of digital circuit boards used inside servers routers and other equipment from high level system architecture down to the low level signal integrity concepts It explains common structures and subsystems that can be expanded into new designs in different markets The book is targeted at all levels of hardware engineers There are shorter lower level introductions to every topic while the book also takes the reader all they way to the most complex and most advanced topics of digital circuit design layout design analysis and hardware architecture

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