3. Balanced Modulator

Aim:

- To construct and properly adjust a balanced modulator and study its operation.
- To observe that the output is a double sideband with a suppressed carrier signal.
- To adjust it for optimum carrier suppression.
- To verify that the output audio level directly affects the double side-band output amplitude.
- 5. To observe that the output is maximum with zero audio input.
- To measure carrier only output and the peak side- band output and to calculate the carrier suppression.

Equipment Required:

Analog communication kit, CRO connecting wires.

Component Required:

- 1. IC'S:1496- (1no)
- Resistors:100Ω-(1no),820Ω-(1no),1kΩ-(1no)1.2kΩ-(1no),2.7kΩ-(1no),10kΩ-(3nos),47kΩ-(1nos),50kΩ-POT
- Capacitors: 1μF-(1no),22μF-(1no),100μF-(2nos)

Theory:

One circuit that lends itself extremely well to balanced modulator application is the differential amplifier. A simplified diagram of a differential amplifier is shown in figure 1.



FIG 1: A Differential amplifier used as a balanced modulator

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synchronization signal to noise ratio analysis and many more All the solutions have step by step approach for easy
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