

Analog Circuits

Important Bits For I MID Exam

1. What is the main function of a diode clipper circuit?

- a) To amplify a signal
- b) To limit the amplitude of a signal
- c) To shift the DC level of a signal
- d) To rectify a signal

2. Which of the following is NOT a common type of diode clipper circuit?

- a) Series clipper
- b) Shunt clipper
- c) Parallel clipper
- d) Combination clipper

3. What is the purpose of a clamping circuit?

- a) To limit the amplitude of a signal
- b) To shift the DC level of a signal
- c) To rectify a signal
- d) To amplify a signal

4. What is the typical output waveform of a clamping circuit?

- a) A sine wave
- b) A square wave
- c) A triangular wave
- d) A sawtooth wave

5. How can a clamping circuit be modified to produce a negative-going output?

- a) By reversing the polarity of the diode
- b) By adding a DC voltage source
- c) By changing the load resistor
- d) By using a different type of diode

DC Biasing of BJTs

6. What is the purpose of DC biasing a BJT?

- a) To set the operating point of the transistor
- b) To amplify the signal
- c) To rectify the signal
- d) To limit the amplitude of the signal

7. What is a load line?

Analog Circuits Objective Questions And Answers

Mr. Rohit Manglik



Analog Circuits Objective Questions And Answers:

ANALOG ELECTRONICS A. KANDASWAMY, ANDRÉ PITTET, 2009-09-01 The recent growth of industrial automation as well as wireless communication has made the Analog Electronics course even more relevant in today's undergraduate programmes This well written text offers a comprehensive introduction to the concepts of circuit analysis electronic devices and analog integrated circuits The primary aim of this textbook is to raise the analytical skills of students required for the analysis and design of analog electronic circuits This book exposes the students to the current trends in Analog Electronics including the complete analysis and design of electronic circuit using Diodes BJTs FETs MOSFETs CMOS and operational amplifiers

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analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck **DIGITAL ELECTRONICS**

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Testing and Diagnosis of Analog Circuits and Systems Ruey-wen Liu, 2012-12-06 IS THE TOPIC ANALOG TESTING AND DIAGNOSIS TIMELY Yes indeed it is Testing and Diagnosis is an important topic and fulfills a vital need for the electronic industry The testing and diagnosis of digital electronic circuits has been successfully developed to the point that it can be automated Unfortunately its development for analog electronic circuits is still in its Stone Age The engineer's intuition is still the most powerful tool used in the industry There are two reasons for this One is that there has been no pressing need from the industry Analog circuits are usually small in size Sometimes the engineer's experience and intuition are sufficient to fulfill the need The other reason is that there are no breakthrough results from academic research to provide the industry with critical ideas to develop tools This is not because of a lack of effort Both academic and industrial research groups have made major efforts to look into this problem Unfortunately the problem for analog circuits is fundamentally different from and much more difficult than its counterpart for digital circuits These efforts have led to some important findings but are still not at the point of being practically useful However these situations are now changing The current trend for the design of VLSI chips is to use analog digital hybrid circuits instead of digital circuits from the past Therefore even though the analog circuit may be small the total circuit under testing is large

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