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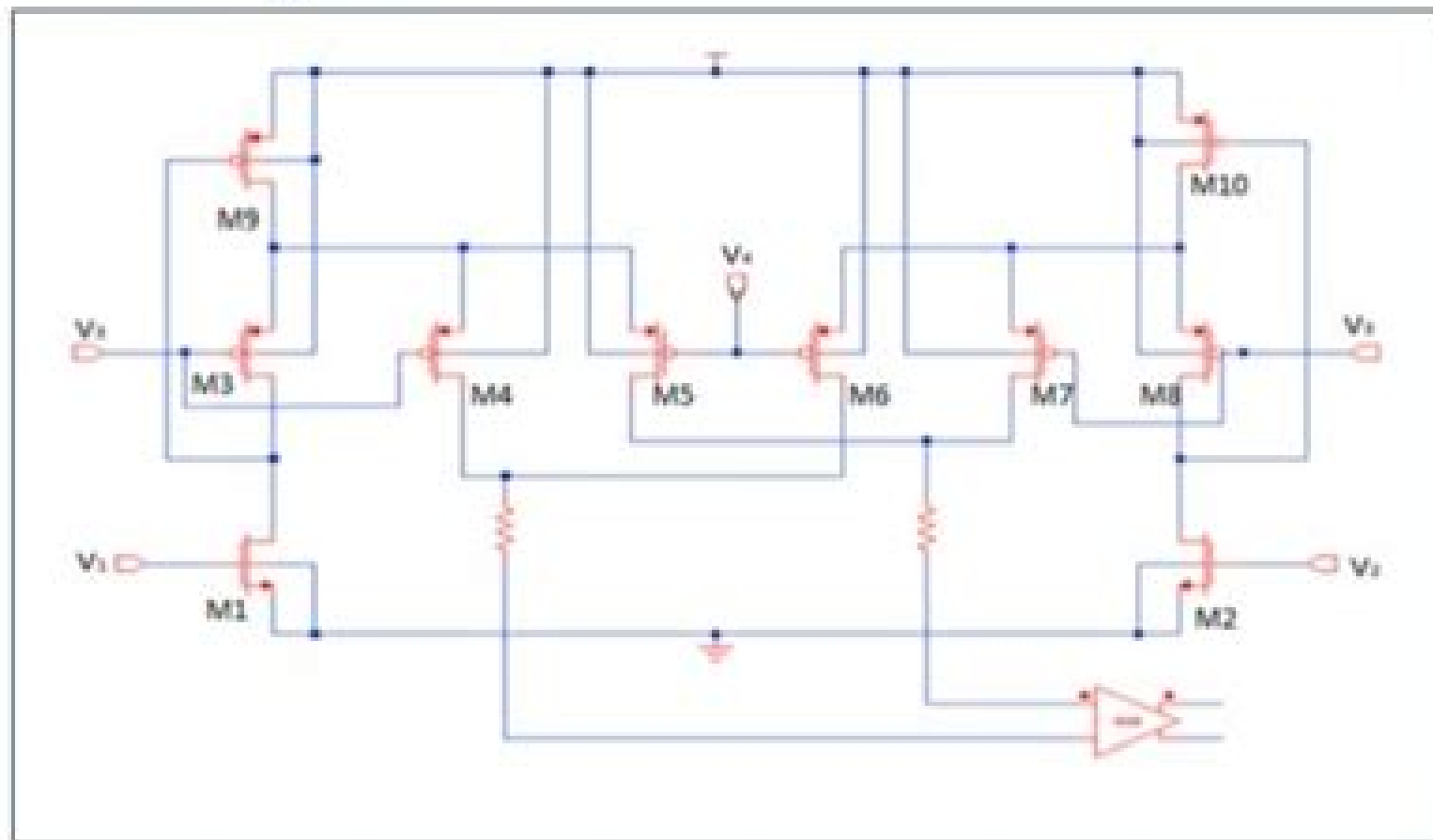


Fig. 6: Analog multiplier based on square rooting circuit^[1]

Analog Multiplier Circuit Cmos Technology

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Acharya,Chanchal Kr. De,Surajit
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